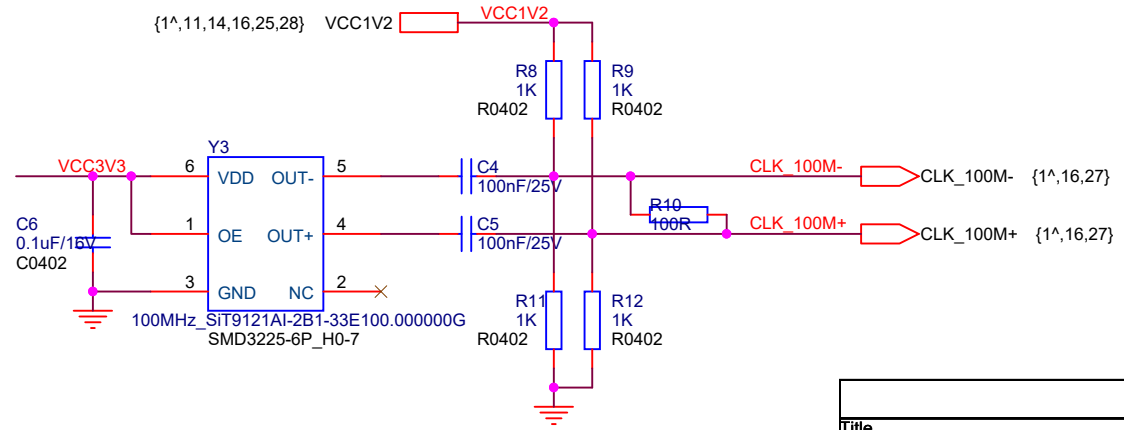
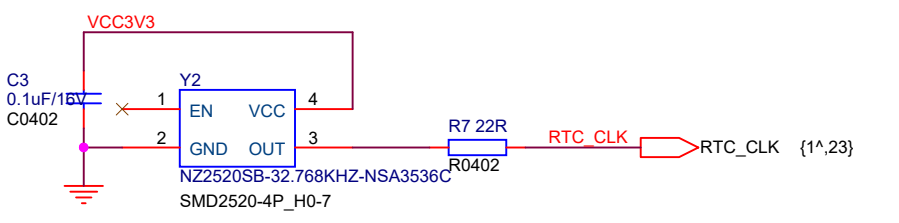
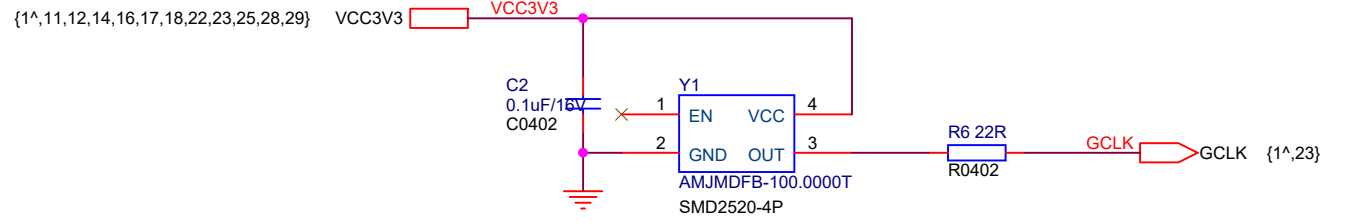
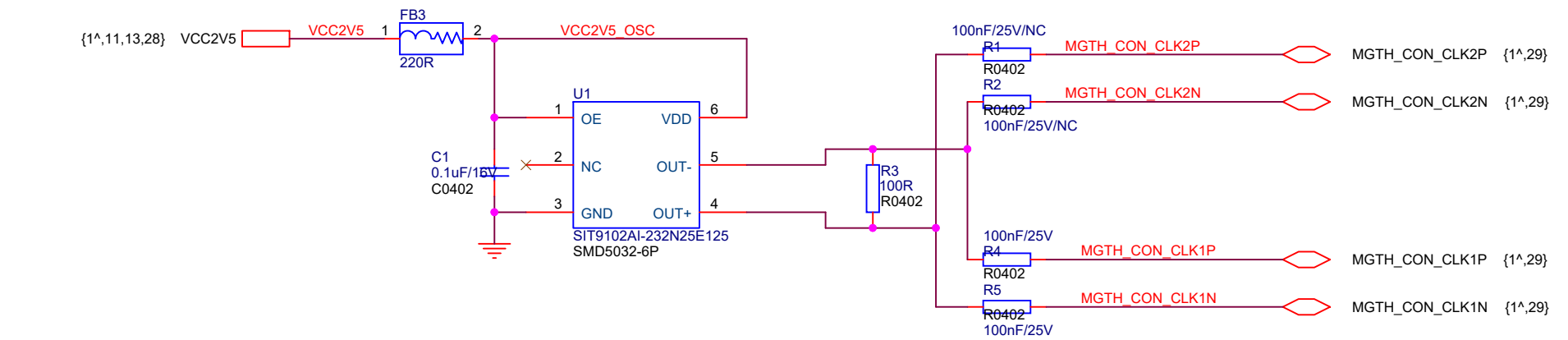
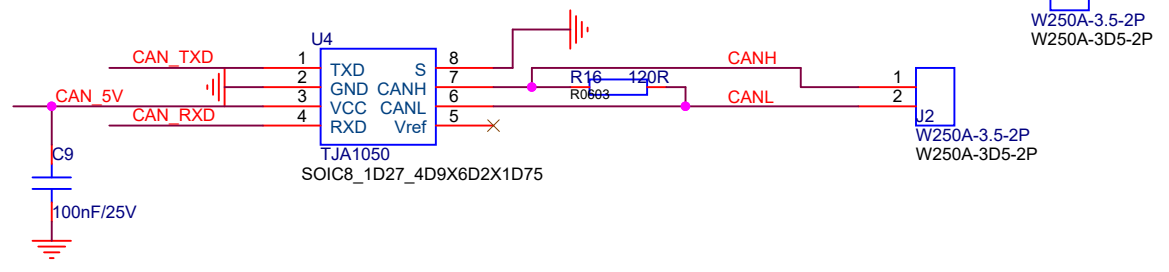
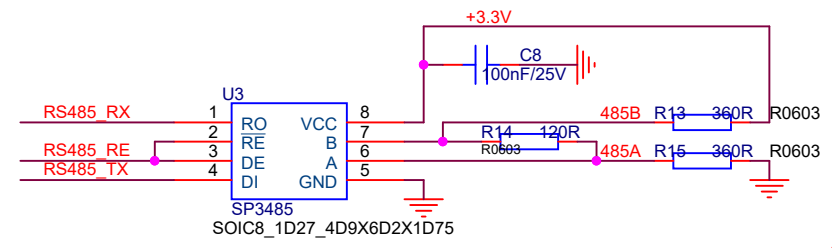
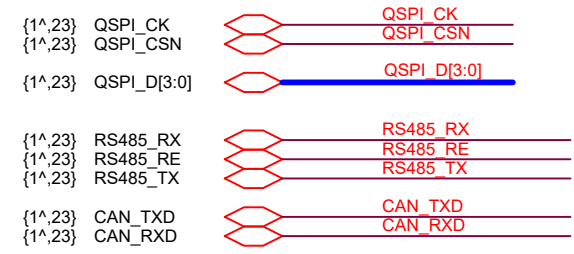
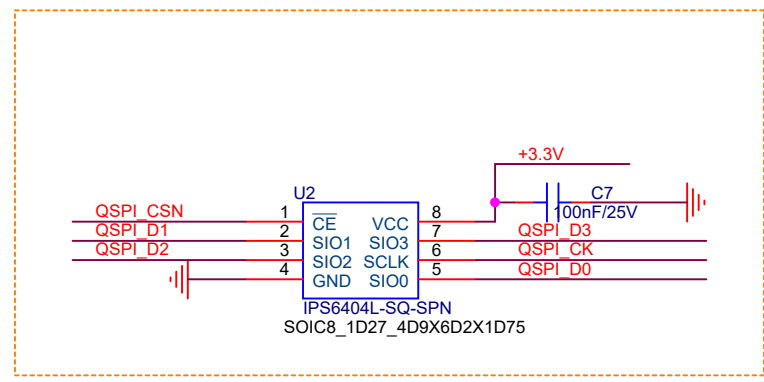
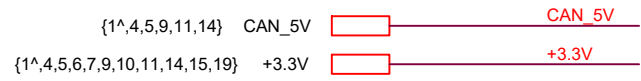


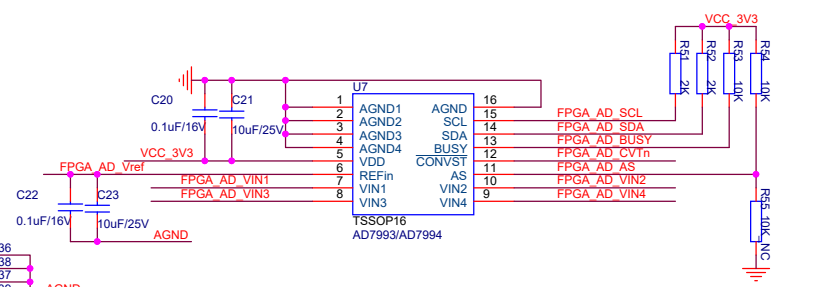
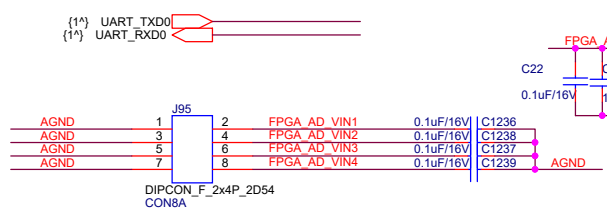
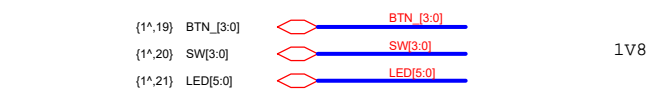
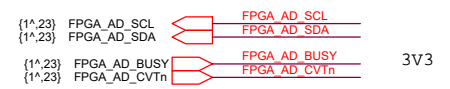
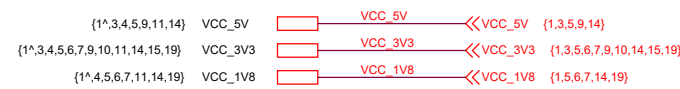
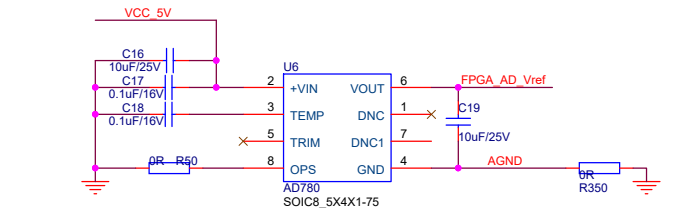
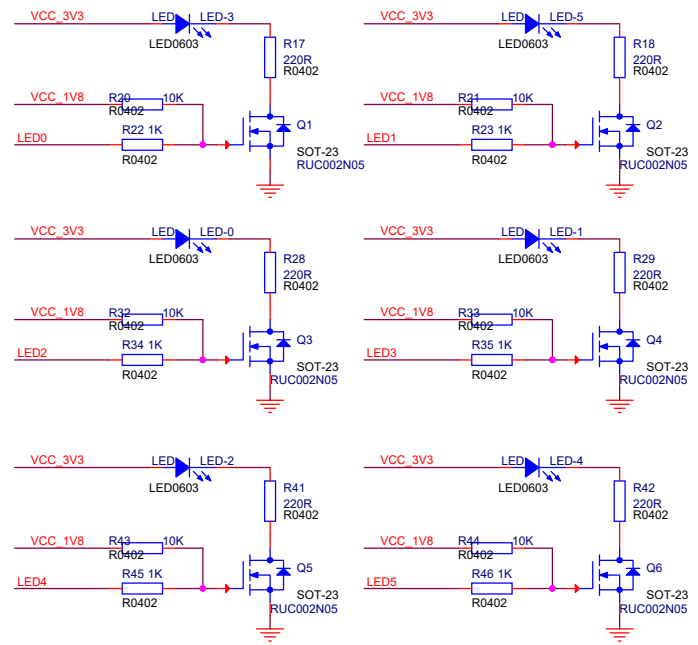
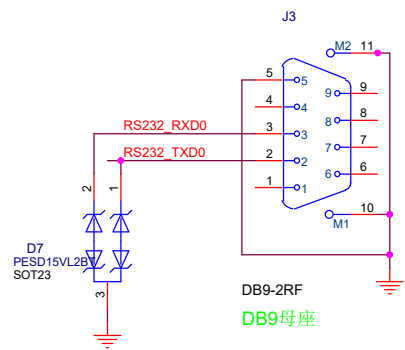
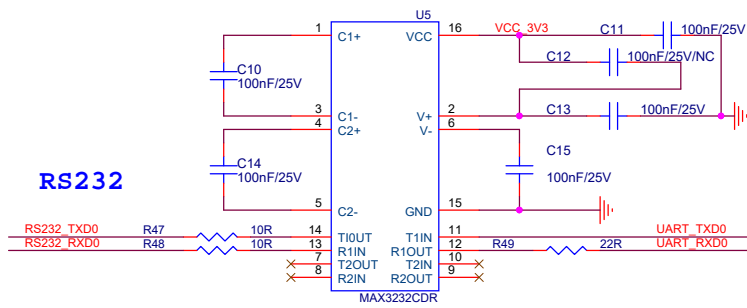
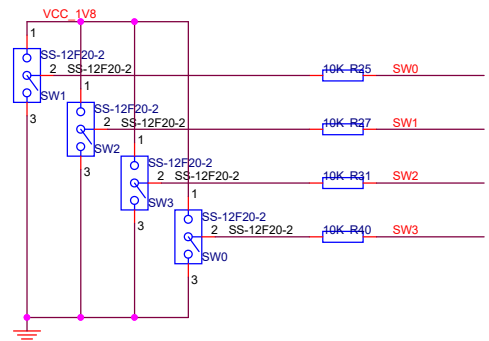
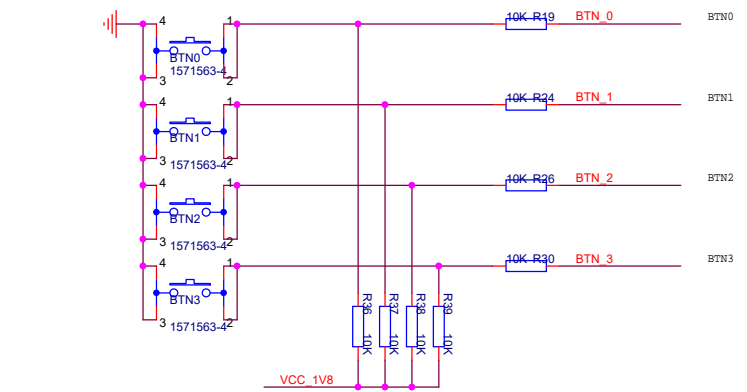
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Size	Document Number	Rev
A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 1 of 28



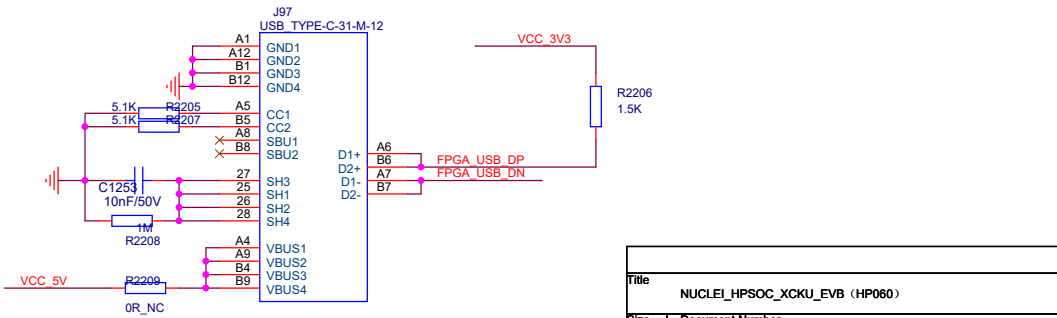
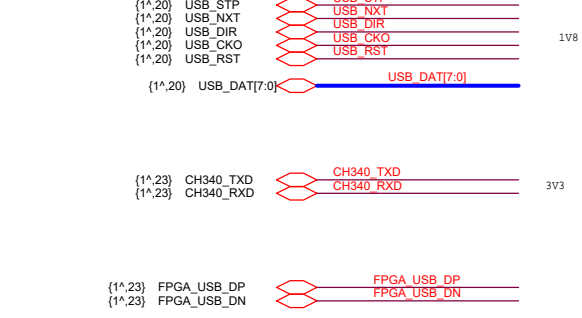
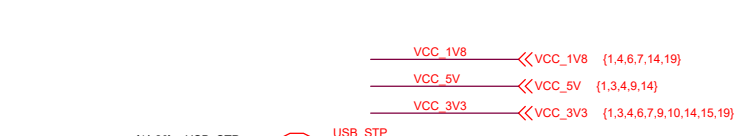
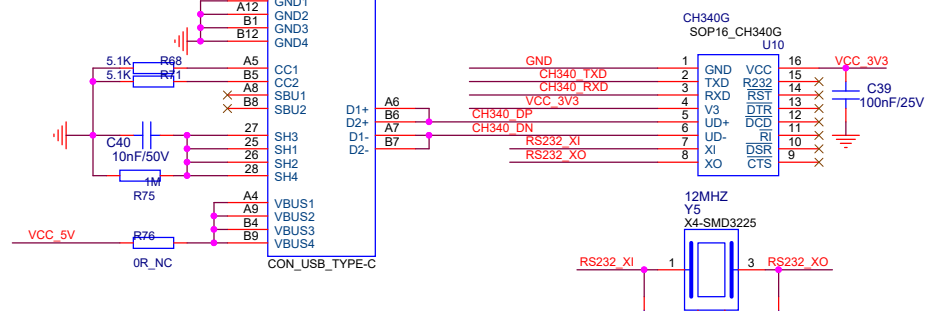
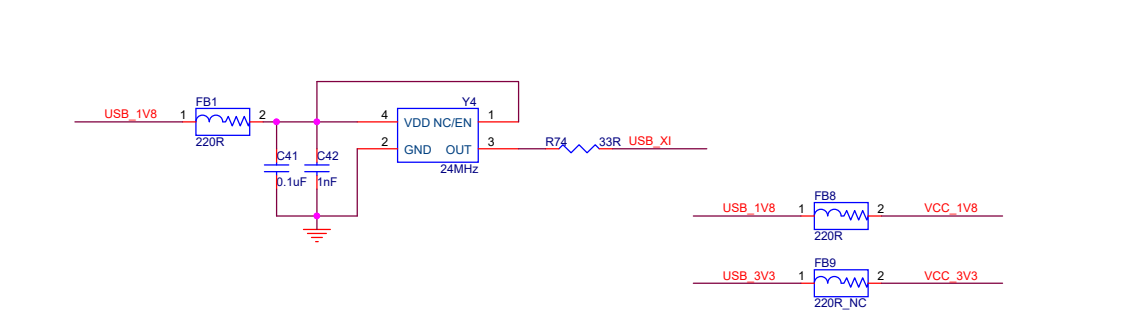
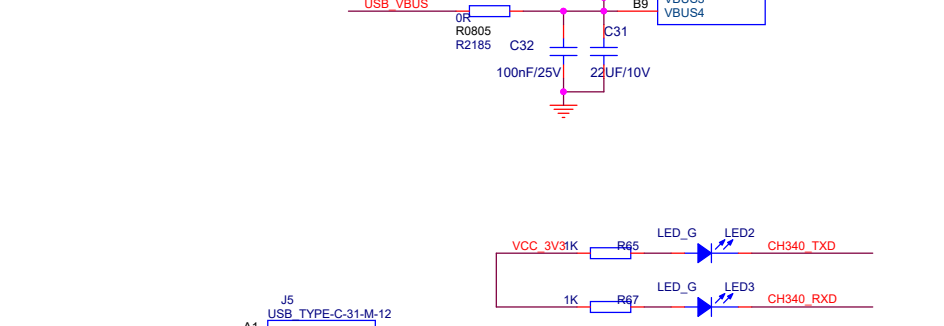
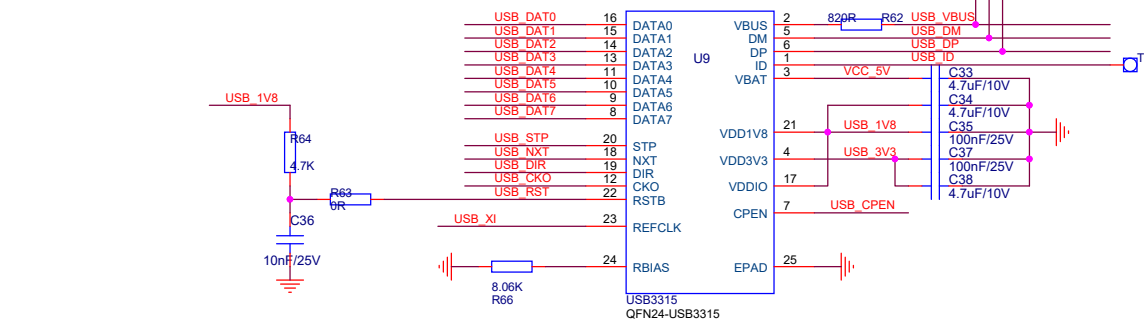
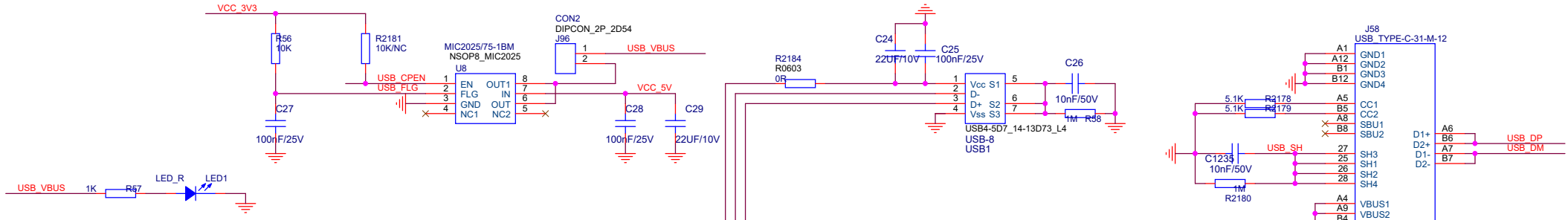
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Size	Document Number	Rev
A4	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 2 of 28



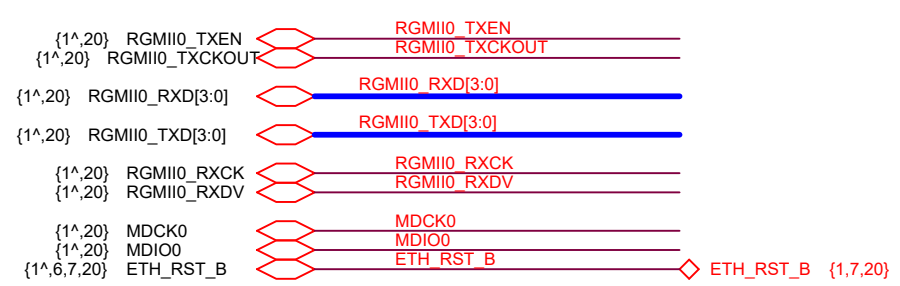
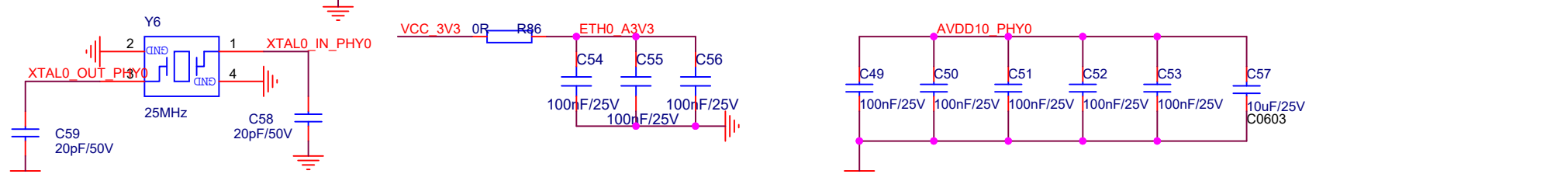
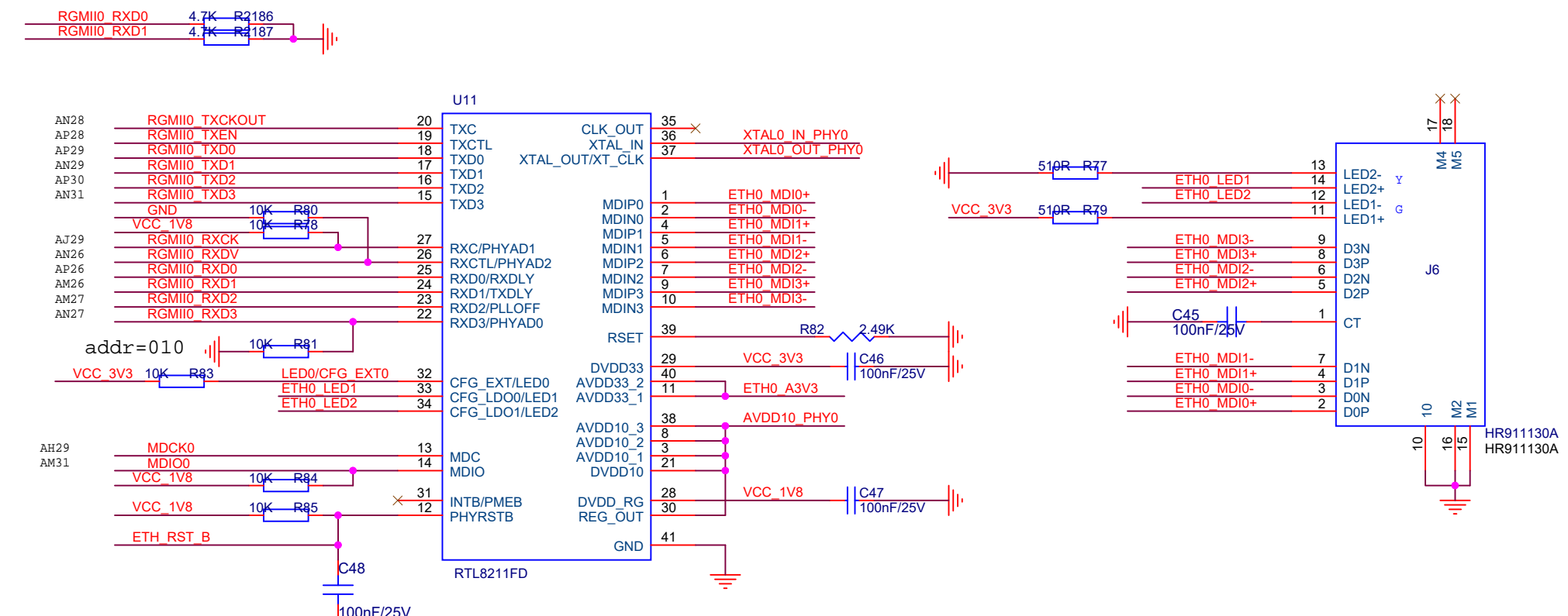
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Date:	Tuesday, October 12, 2021	Sheet 3 of 28



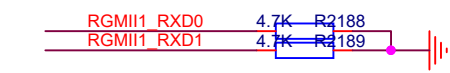
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A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 4 of 28



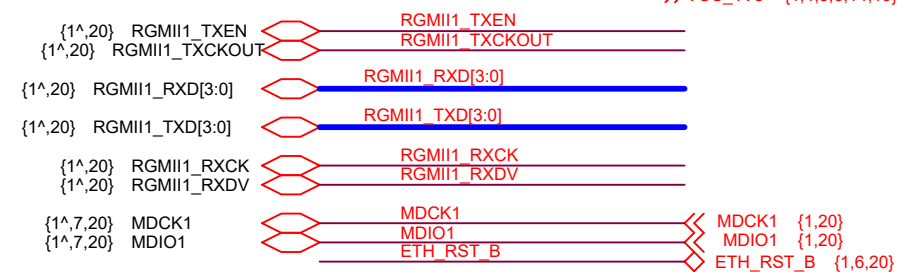
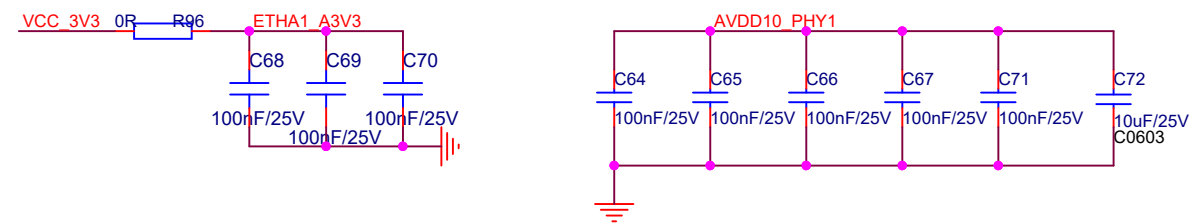
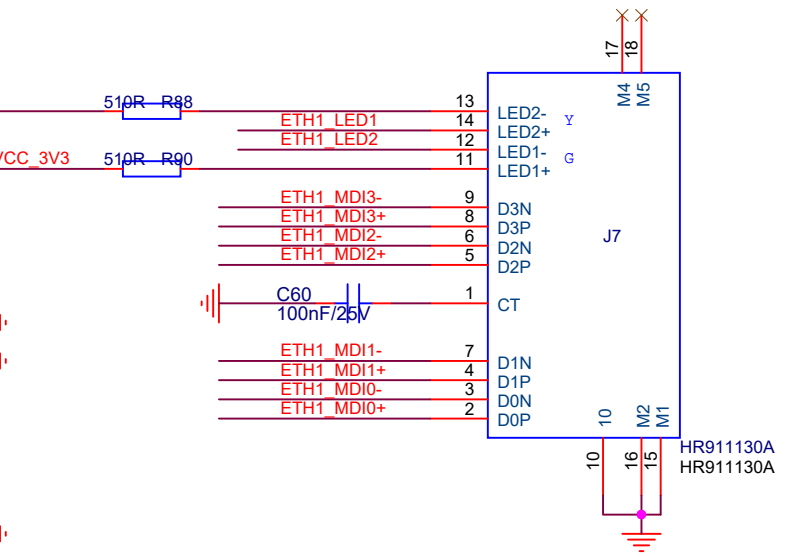
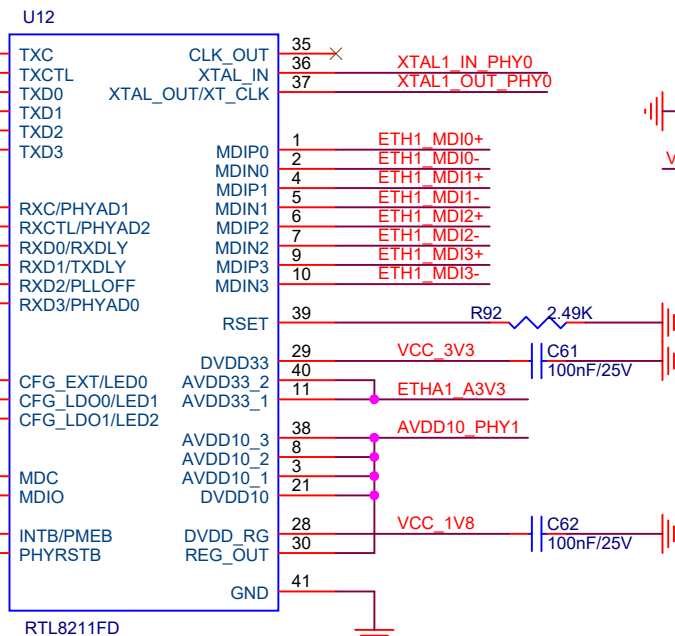
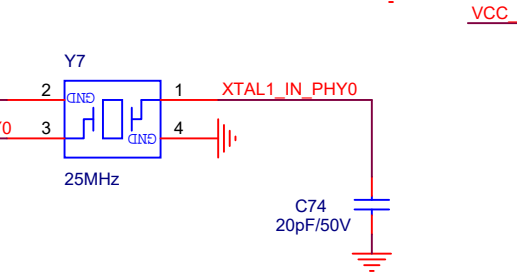
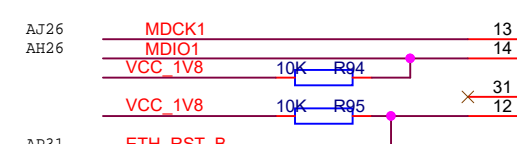
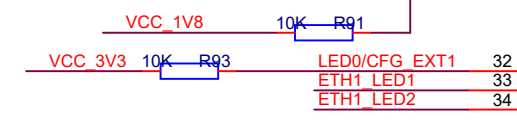
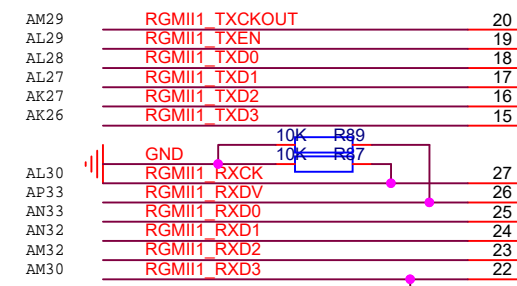
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A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 5 of 28



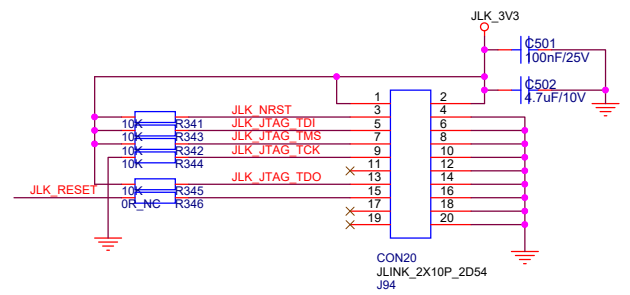
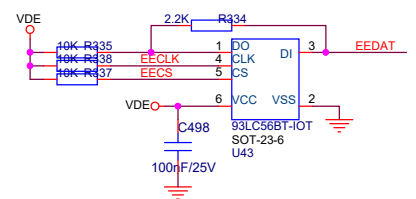
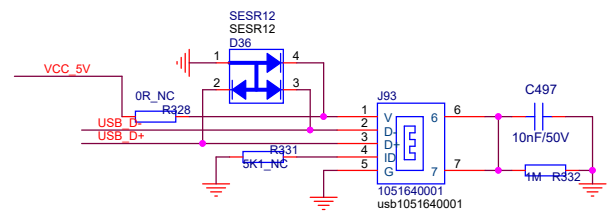
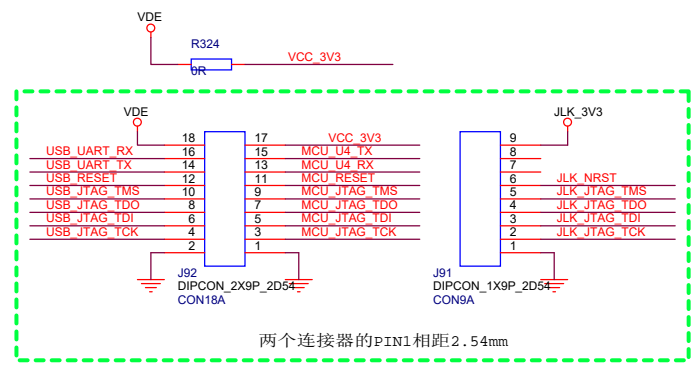
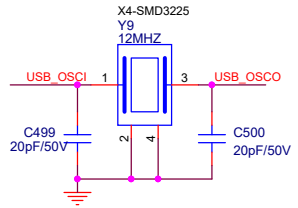
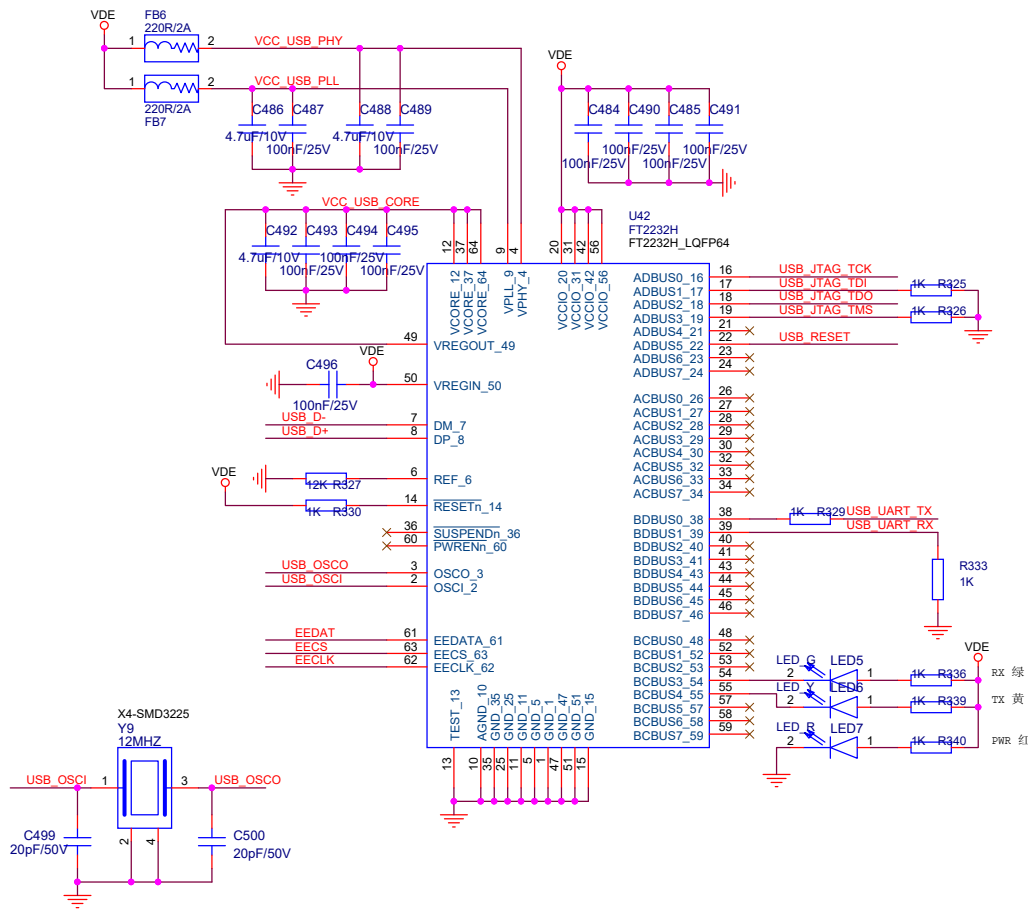
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Size	Document Number	Rev
A4	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 6 of 28



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Title		
NUCLEI_HPSOC_XCKU_EVB (HP060)		
Size	Document Number	Rev
A4	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 7 of 28



VCC\_5V << VCC\_5V {1,3,4,5,14}  
 VCC\_3V3 >> VCC\_3V3 {1,3,4,5,6,7,10,14,15,19}

- MCU\_JTAG\_TMS <> MCU\_JTAG\_TMS {1^,23}
- MCU\_JTAG\_TDO <> MCU\_JTAG\_TDO {1^,23}
- MCU\_JTAG\_TDI <> MCU\_JTAG\_TDI {1^,23}
- MCU\_JTAG\_TCK <> MCU\_JTAG\_TCK {1^,23}
- MCU\_RESET <> MCU\_RESET {1^,15,23}
- JLK\_RESET <> JLK\_RESET {1^,23}
- MCU\_U4\_TX <> MCU\_U4\_TX {1^,23}
- MCU\_U4\_RX <> MCU\_U4\_RX {1^,23}

Title		
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Size	Document Number	Rev
A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 8 of 28





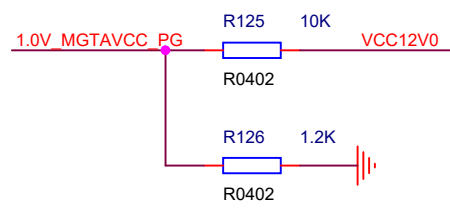
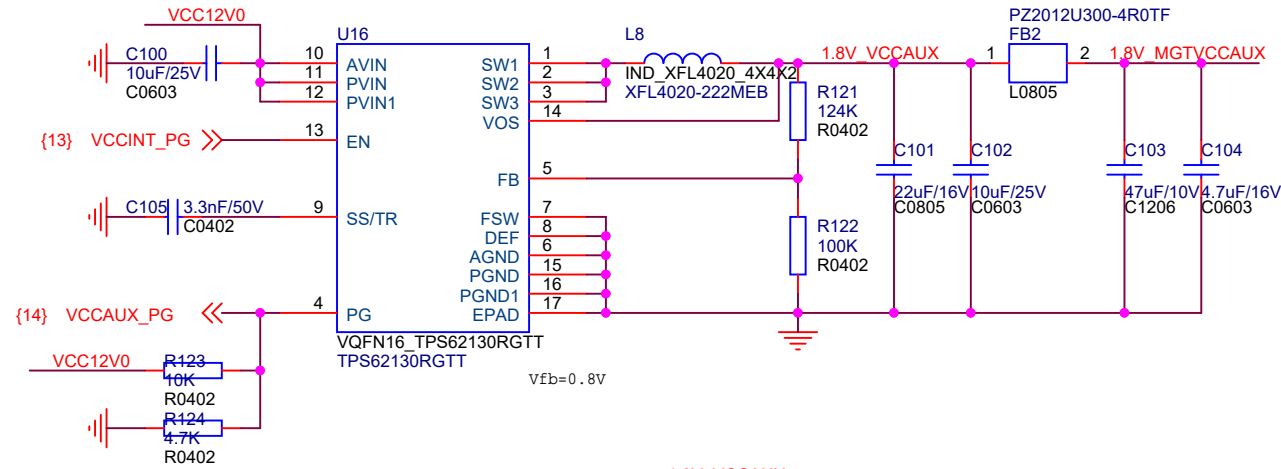
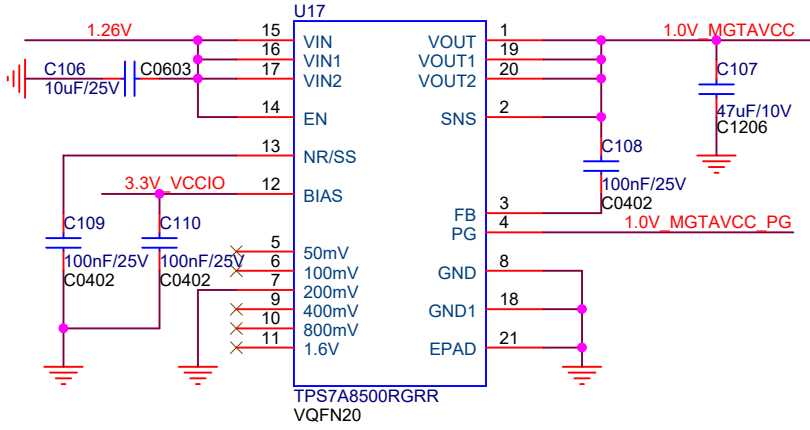
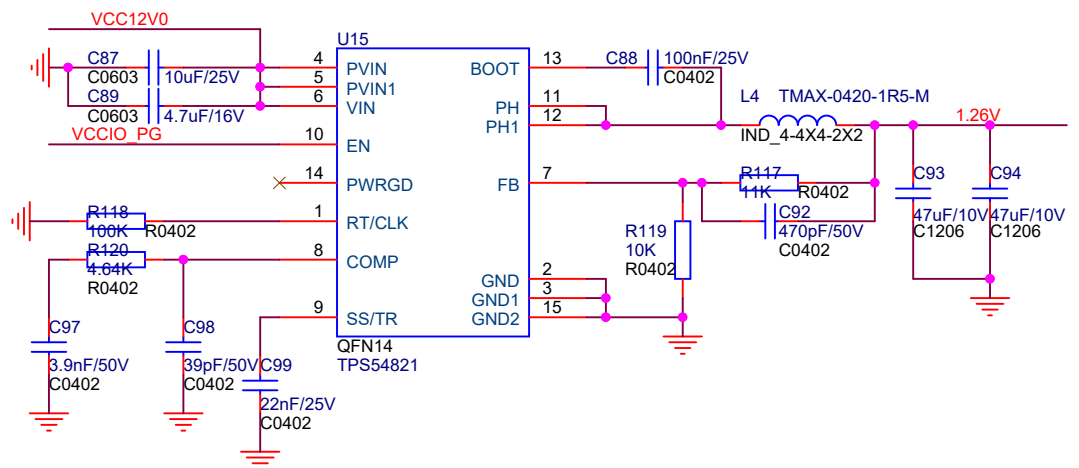
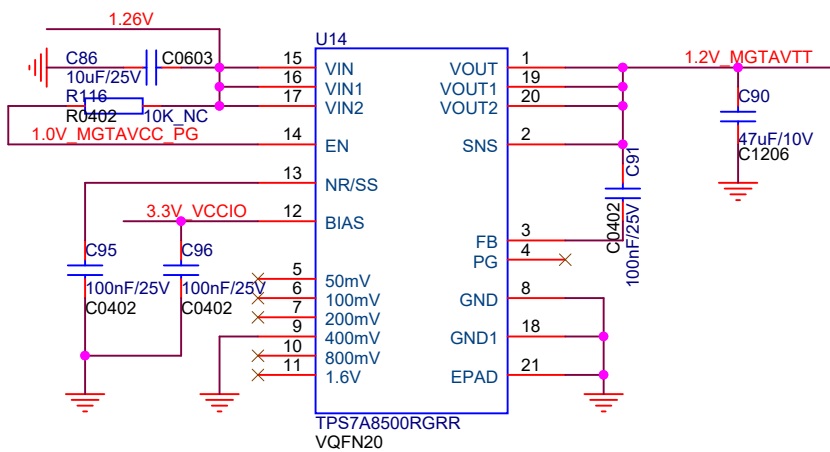
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 {1,12,18,25} 1.8V\_VCCAUX << 1.8V\_VCCAUX 1.8V\_VCCAUX {1^,11,12,16,18,25}  
 {1,12,24} 1.8V\_MGTVCCAUX << 1.8V\_MGTVCCAUX 1.8V\_MGTVCCAUX {1^,11,12,16,24}  
 {1,12,24} 1.0V\_MGTAVCC << 1.0V\_MGTAVCC 1.0V\_MGTAVCC {1^,11,12,16,24}  
 {1,12,24} 1.2V\_MGTAVTT << 1.2V\_MGTAVTT 1.2V\_MGTAVTT {1^,11,12,16,24}  
 {1,13,18,25} 0.95V\_VCCINT << 0.95V\_VCCINT 0.95V\_VCCINT {1^,11,13,16,18,25}  
 {1,2,13,28} 2.5V\_VPP << 2.5V\_VPP 2.5V\_VPP {1^,2,11,13,28}

{1,2,14,25,28} 1.2V\_VCCIO << 1.2V\_VCCIO 1.2V\_VCCIO {1^,2,11,14,16,25,28}  
 {1,14,25} 1.8V\_VCCIO1 << 1.8V\_VCCIO1 1.8V\_VCCIO1 {1^,11,14,16,25}  
 {1,14,22,25} 1.8V\_VCCIO2 << 1.8V\_VCCIO2 1.8V\_VCCIO2 {1^,11,14,16,22,25}  
 {1,2,12,14,17,18,22,23,25,28,29} 3.3V\_VCCIO << 3.3V\_VCCIO 3.3V\_VCCIO {1^,2,11,12,14,16,17,18,22,23,25,28,29}

{1,3,4,5,6,7,9,10,14,15,19} VCC\_3V3 << VCC\_3V3 VCC\_3V3 {1^,3,4,5,6,7,9,10,11,14,15,19}  
 {1,3,4,5,9,14} VCC\_5V << VCC\_5V VCC\_5V {1^,3,4,5,9,11,14}  
 {1,4,5,6,7,14,19} VCC\_1V8 << VCC\_1V8 VCC\_1V8 {1^,4,5,6,7,11,14,19}

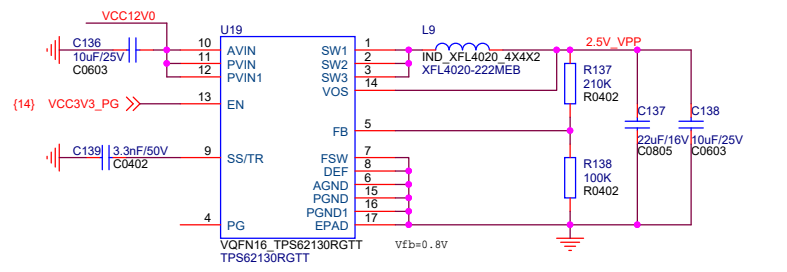
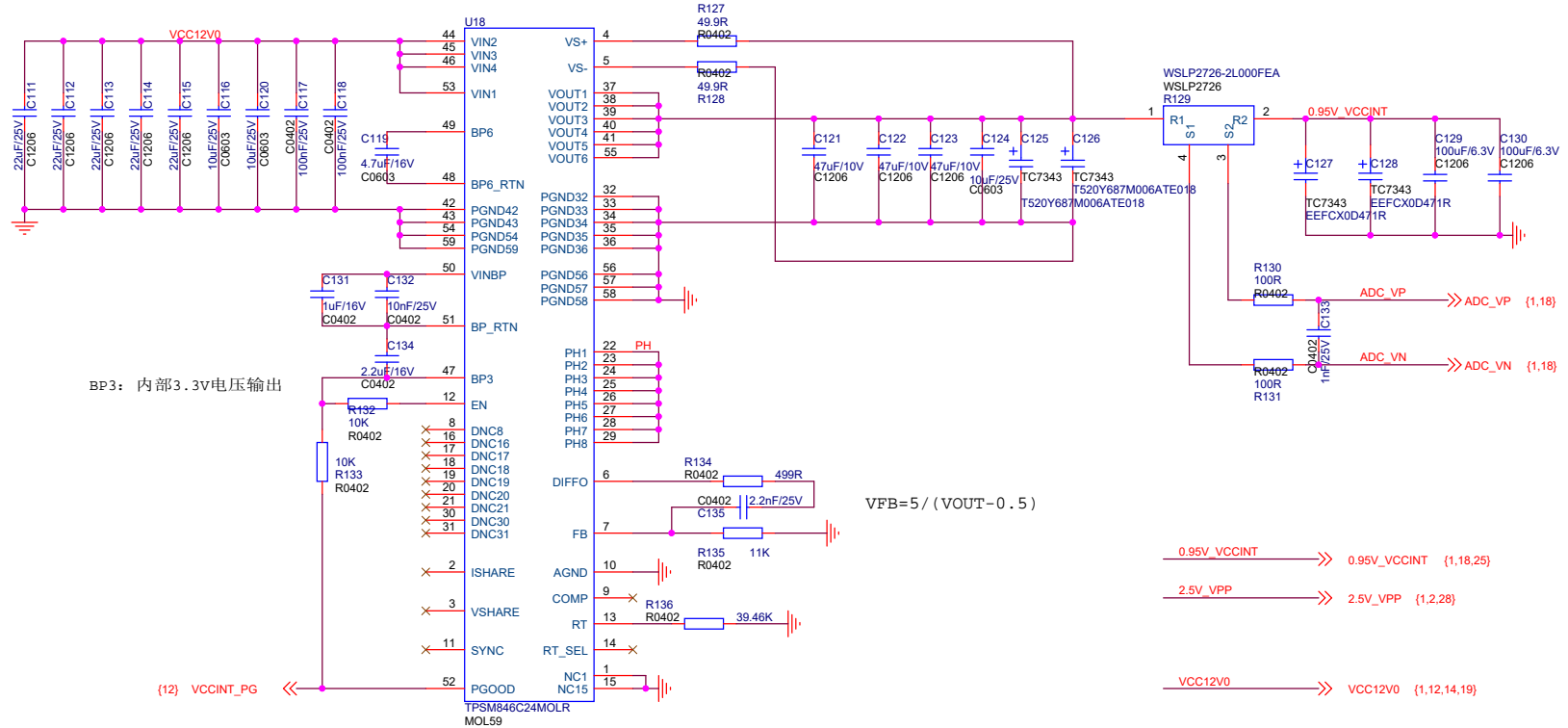
{1,13,18} ADC\_VP << ADC\_VP ADC\_VP {1^,11,13,18}  
 {1,13,18} ADC\_VN << ADC\_VN ADC\_VN {1^,11,13,18}

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NUCLEI_HPSOC_XCKU_EVB (HP060)		
Size A	Document Number <Doc>	Rev 1
Date:	Tuesday, October 12, 2021	Sheet 10 of 28

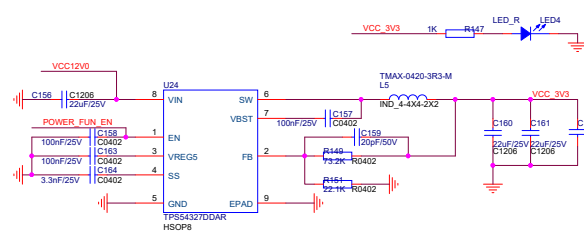
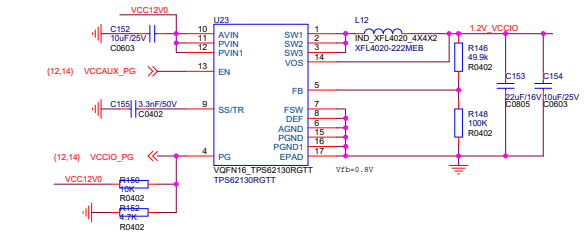
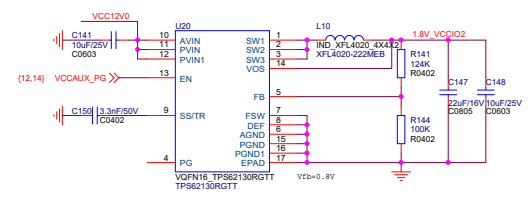
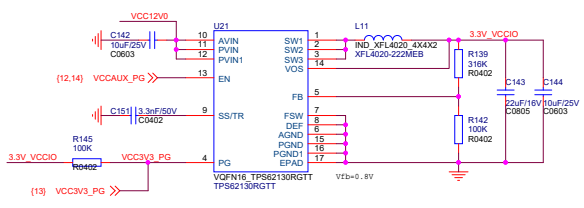


- {1,18,25} 1.8V\_VCC\_AUX << 1.8V\_VCC\_AUX
- {1,24} 1.8V\_MGTA VCC\_AUX << 1.8V\_MGTA VCC\_AUX
- {1,24} 1.0V\_MGTA VCC << 1.0V\_MGTA VCC
- {1,24} 1.2V\_MGTA VTT << 1.2V\_MGTA VTT
- {1,13,14,19} VCC12V0 << VCC12V0
- {14} VCCIO\_PG << VCCIO\_PG
- {1,2,14,17,18,22,23,25,28,29} 3.3V\_VCCIO << 3.3V\_VCCIO

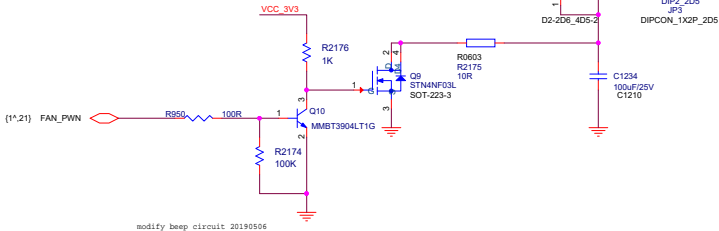
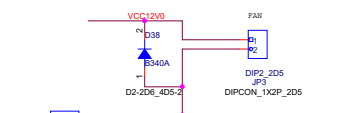
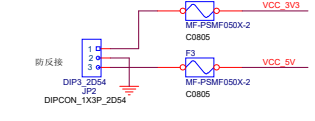
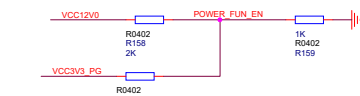
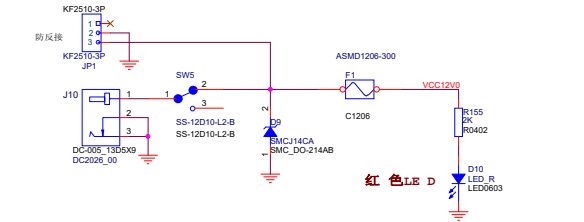
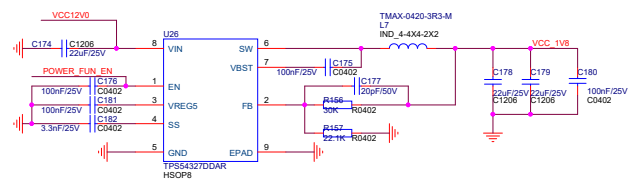
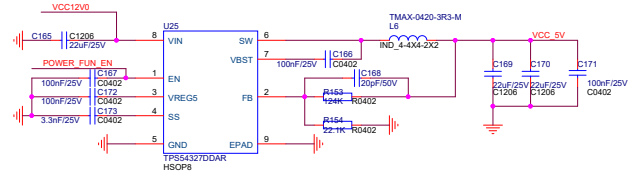
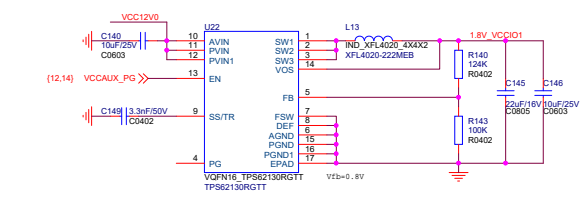
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Date:	Tuesday, October 12, 2021	Sheet 11 of 28



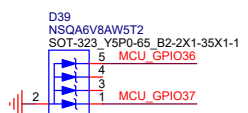
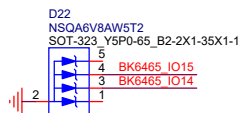
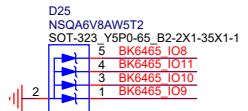
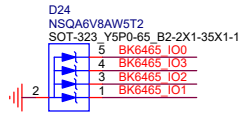
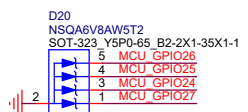
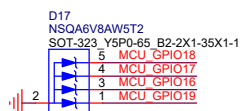
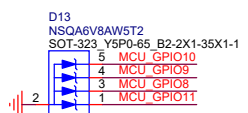
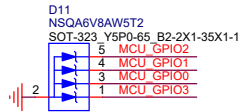
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Size	Document Number	Rev
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Date:	Tuesday, October 12, 2021	Sheet 12 of 28



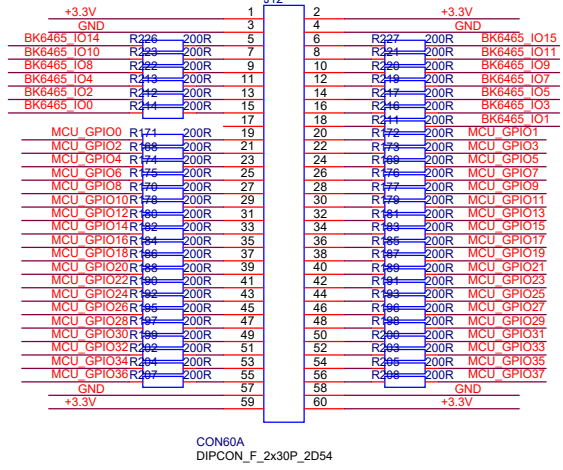
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- VCC12V0 >> VCC12V0 (1.12,13,19)
- 1.2V\_VCCIO >> 1.2V\_VCCIO (1.2,25,28)
- 1.8V\_VCCIO1 >> 1.8V\_VCCIO1 (1.25)
- 1.8V\_VCCIO2 >> 1.8V\_VCCIO2 (1.22,25)
- 3.3V\_VCCIO >> 3.3V\_VCCIO (1.2,12,17,18,22,23,25,28,29)
- VCC\_3V3 >> VCC\_3V3 (1.3,4,5,6,7,8,10,15,19)
- VCC\_5V >> VCC\_5V (1.3,4,5,9)
- VCC\_1V8 >> VCC\_1V8 (1.4,5,6,7,19)



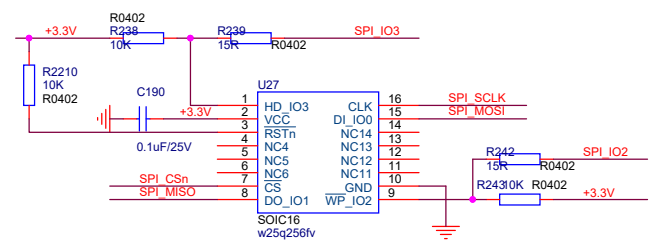
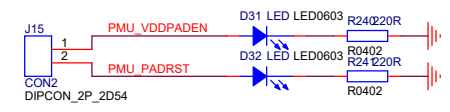
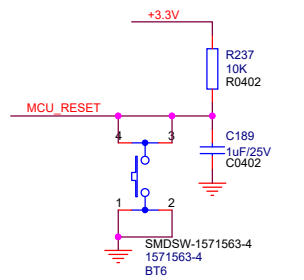
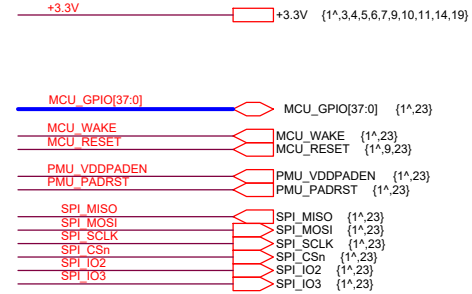
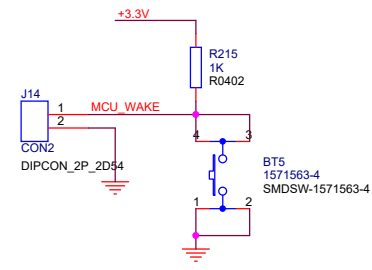
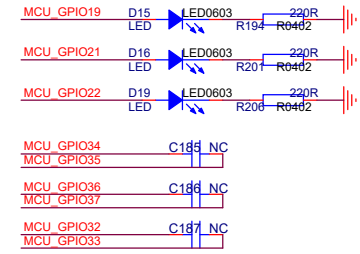
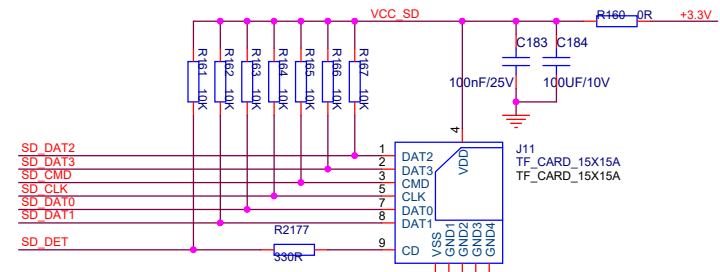
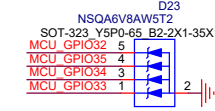
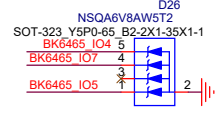
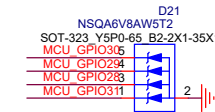
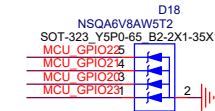
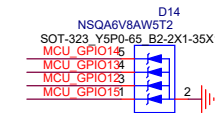
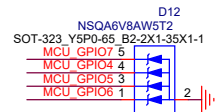
modify beep circuit 20190506



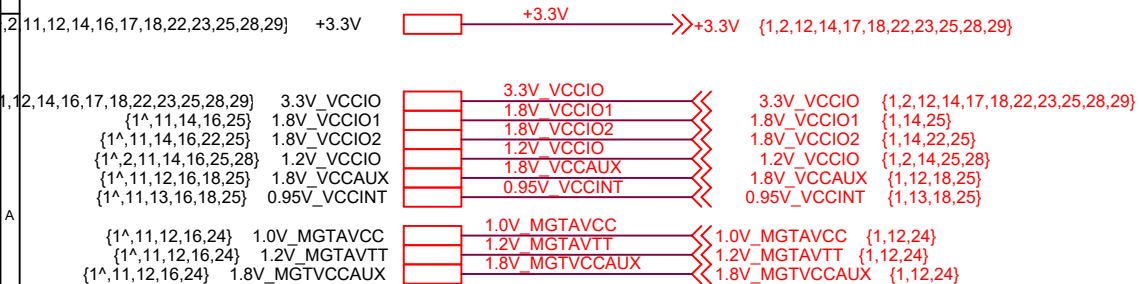
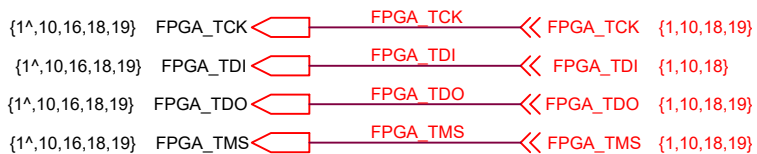
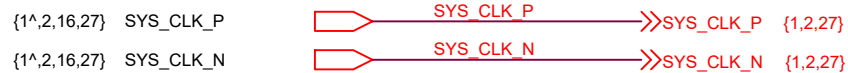
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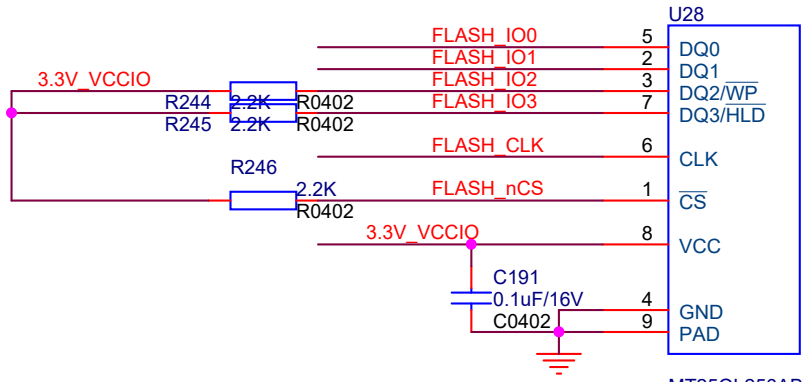
CON60A  
DIPCON\_F\_2x30P\_2D54



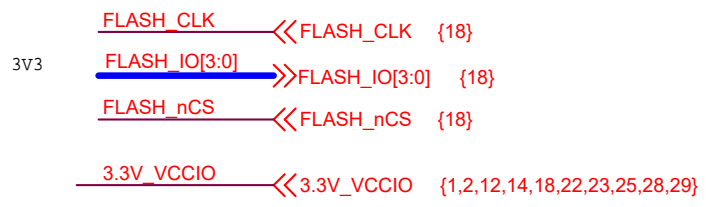
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Size A3	Document Number <Doc>	Rev 1
Date: Tuesday, October 12, 2021	Sheet 14	of 28



Title		
NUCLEI_HPSOC_XCKU_EVB (HP060)		
Size	Document Number	Rev
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Date:	Tuesday, October 12, 2021	Sheet 15 of 28

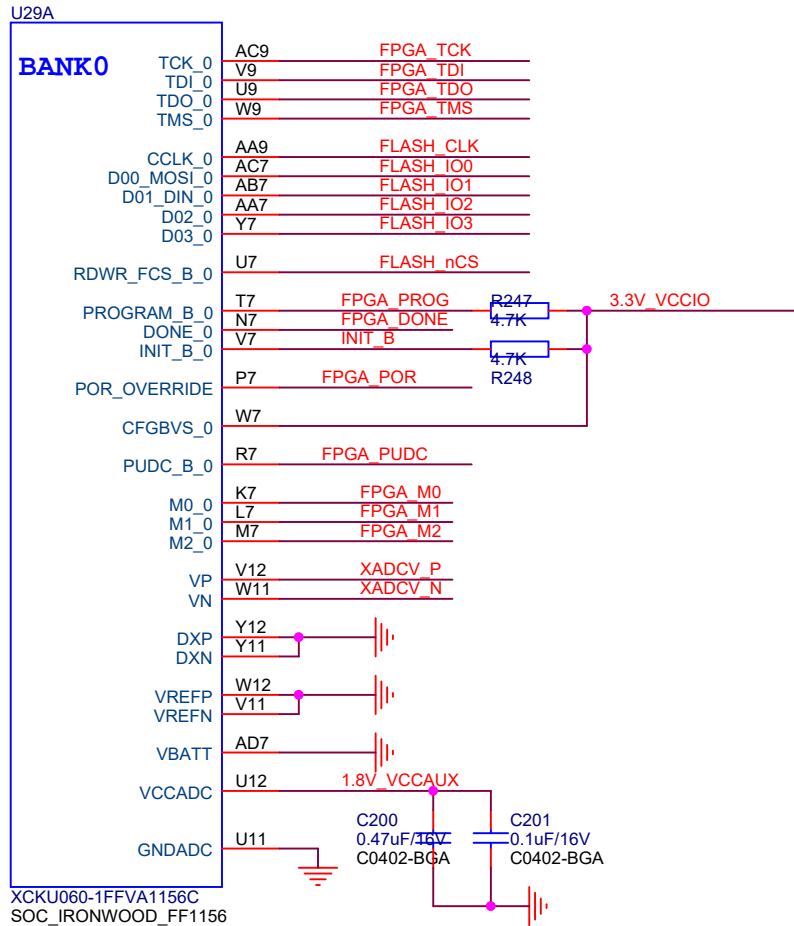


U28  
 5 FLASH IO0  
 2 DQ0  
 3 DQ1  
 7 DQ2/ $\overline{WP}$   
 DQ3/HLD  
 6 FLASH CLK  
 CLK  
 1  $\overline{CS}$   
 $\overline{CS}$   
 8 3.3V\_VCCIO  
 VCC  
 4 C191  
 0.1uF/16V  
 9 C0402  
 GND PAD  
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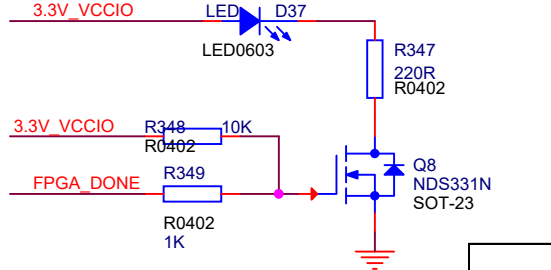
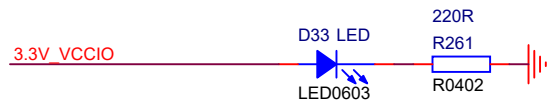
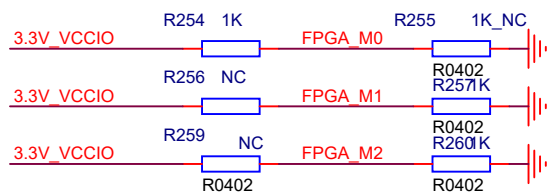
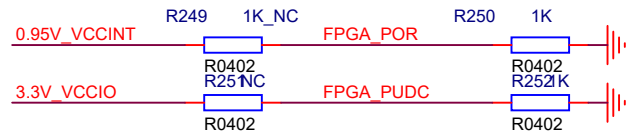
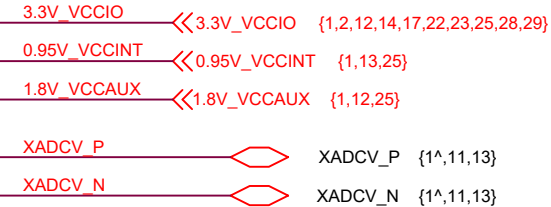
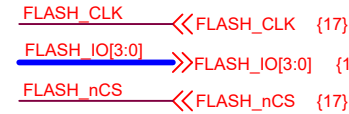
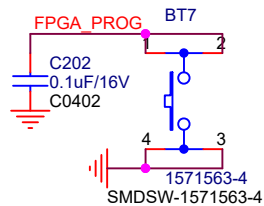
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Size	Document Number	Rev
A	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 16 of 28



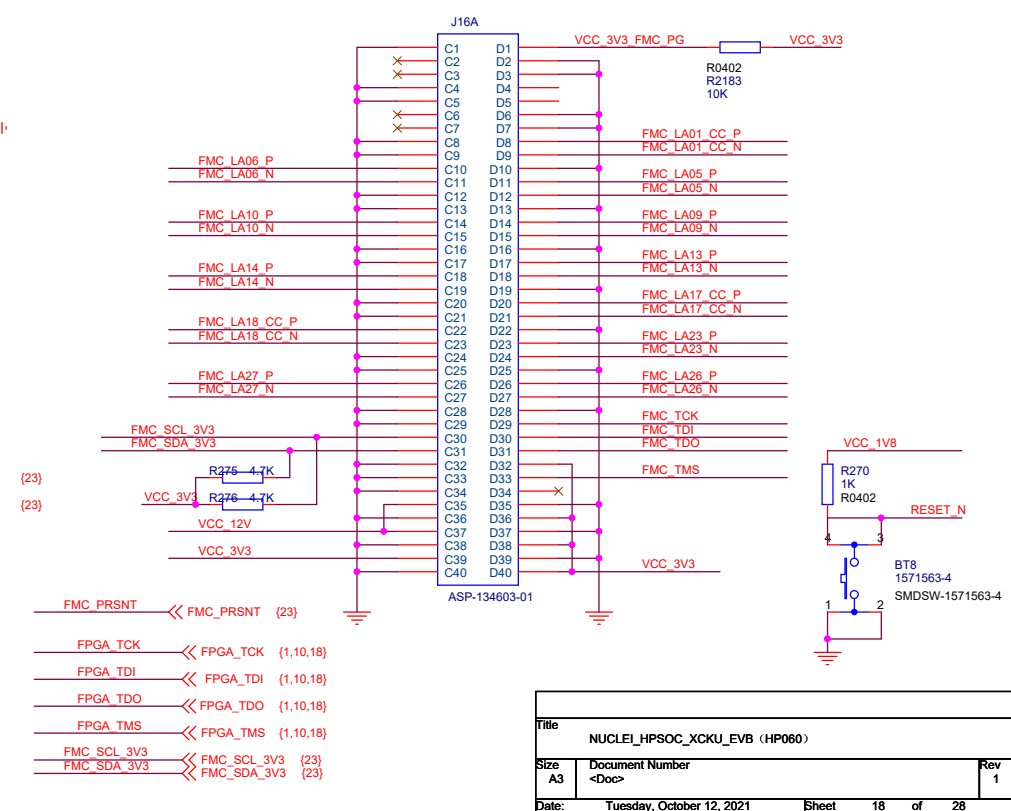
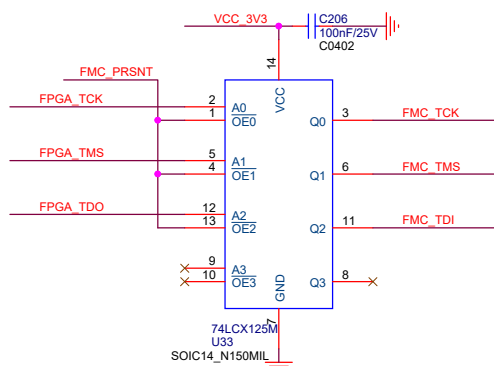
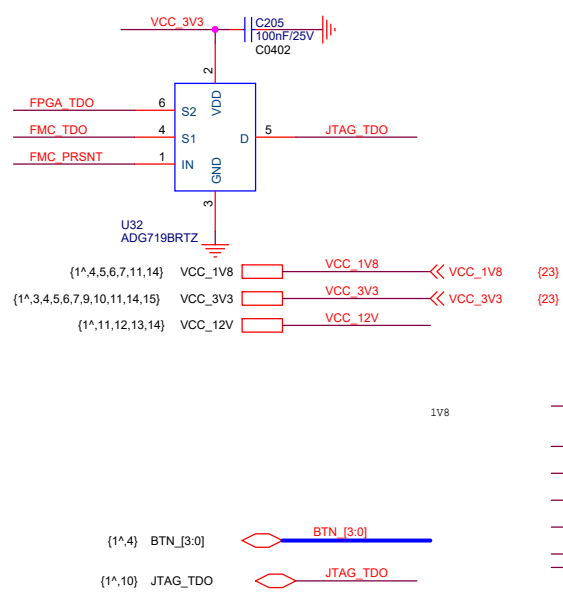
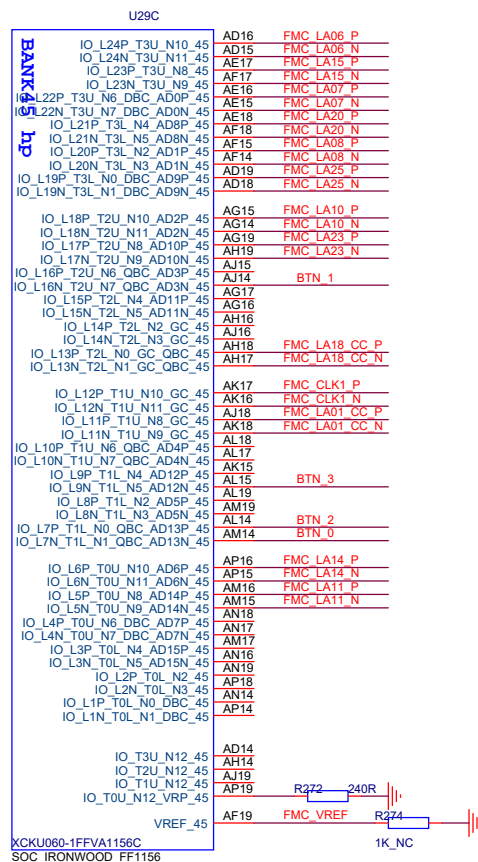
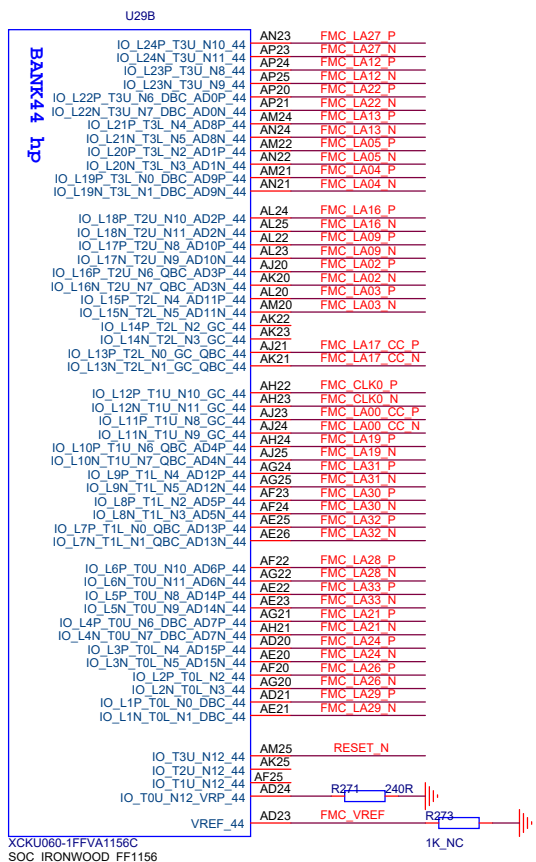


电池组电压  
 只有在使用位流加密时才需要VBATT 如果不使用电池  
 将VBATT连接到地面或VCCAUX

CFGBVS\_0: 用于配置BANK0和BANK65 的电压工作范围  
 若VCCO\_0=2.5V or 3.3V,CFGBVS\_0为高接VCCO\_0  
 若VCCO\_0=1.5V or 1.8V,CFGBVS\_0为低接GND



Title		
NUCLEI_HPSOC_XCKU_EVB (HP060)		
Size	Document Number	Rev
A4	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 17 of 28



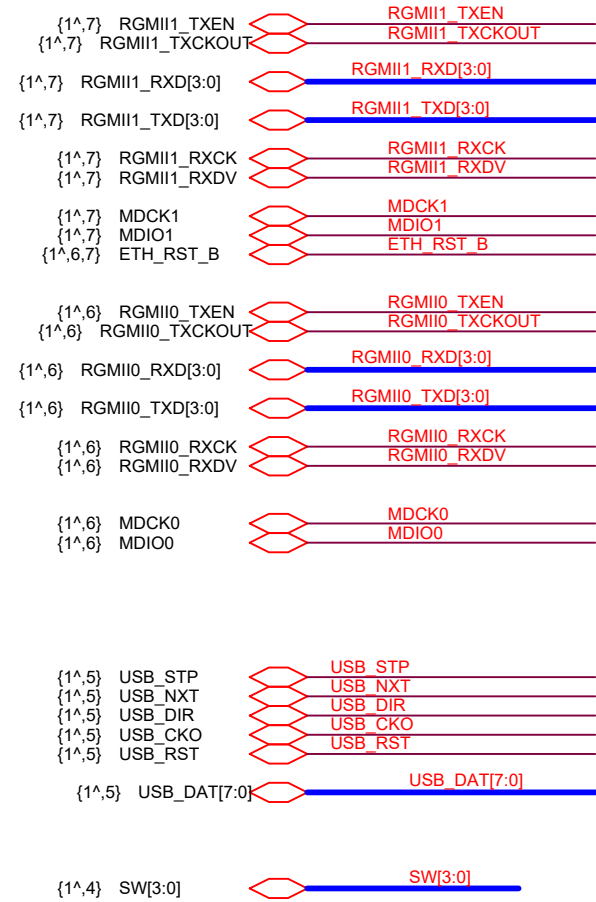
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NUCLEI_HPSOC_XCKU_EV8 (HP060)		
Size	Document Number	Rev
A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 18 of 28

U29D

BANK 46 HP

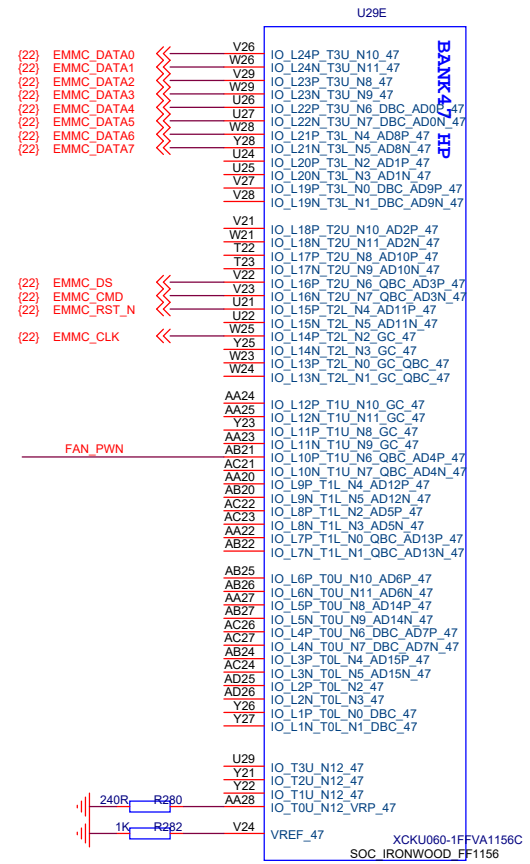
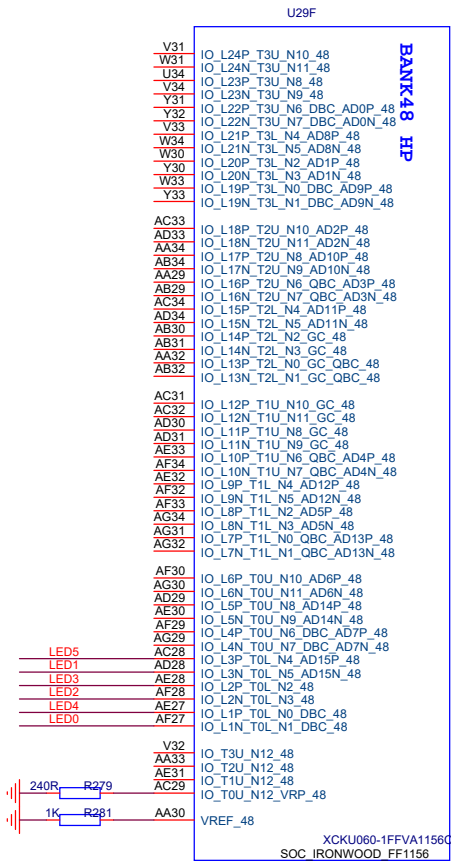
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	MDIO1	AM34	IO_L24N_T3U_N11_46
AM32	RGMI1_RXD2	AM32	IO_L23P_T3U_N8_46
AN32	RGMI1_RXD1	AN32	IO_L23N_T3U_N9_46
	ETH_RST_B	AN34	IO_L22P_T3U_N6_DBC_AD0P_46
	MDIO0	AP34	IO_L22N_T3U_N7_DBC_AD0N_46
AN31	RGMI0_TXD3	AN31	IO_L21P_T3L_N4_AD8P_46
AP31	MDCK0	AP31	IO_L21N_T3L_N5_AD8N_46
AN33	RGMI1_RXD0	AN33	IO_L20P_T3L_N2_AD1P_46
AP33	RGMI1_RXDV	AP33	IO_L20N_T3L_N3_AD1N_46
	USB_DAT4	AL32	IO_L19P_T3L_N0_DBC_AD9P_46
	USB_DAT6	AL33	IO_L19N_T3L_N1_DBC_AD9N_46
	USB_RST	AH34	IO_L18P_T2U_N10_AD2P_46
	USB_DAT5	AJ34	IO_L18N_T2U_N11_AD2N_46
	USB_DAT2	AH31	IO_L17P_T2U_N8_AD10P_46
	USB_DIR	AH32	IO_L17N_T2U_N9_AD10N_46
	USB_STP	AH33	IO_L16P_T2U_N6_QBC_AD3P_46
	USB_DAT3	AJ33	IO_L16N_T2U_N7_QBC_AD3N_46
		AJ30	IO_L15P_T2L_N4_AD11P_46
		AJ31	IO_L15N_T2L_N5_AD11N_46
	USB_CKO	AK31	IO_L14P_T2L_N2_GC_46
		AK32	IO_L14N_T2L_N3_GC_46
AJ29	RGMI0_RXCK	AJ29	IO_L13P_T2L_N0_GC_QBC_46
		AK30	IO_L13N_T2L_N1_GC_QBC_46
AL30	RGMI1_RXCK	AL30	IO_L12P_T1U_N10_GC_46
AM30	RGMI1_RXD3	AM30	IO_L12N_T1U_N11_GC_46
AL29	RGMI1_TXEN	AL29	IO_L11P_T1U_N8_GC_46
AM29	RGMI1_TXCKOUT	AM29	IO_L11N_T1U_N9_GC_46
AN29	RGMI0_TXD1	AN29	IO_L10P_T1U_N6_QBC_AD4P_46
AP30	RGMI0_TXD2	AP30	IO_L10N_T1U_N7_QBC_AD4N_46
AN27	RGMI0_RXD3	AN27	IO_L9P_T1L_N4_AD12P_46
AN28	RGMI0_TXCKOUT	AN28	IO_L9N_T1L_N5_AD12N_46
AP28	RGMI0_TXEN	AP28	IO_L8P_T1L_N2_AD5P_46
AP29	RGMI0_TXD0	AP29	IO_L8N_T1L_N3_AD5N_46
AN26	RGMI0_RXDV	AN26	IO_L7P_T1L_N0_QBC_AD13P_46
AP26	RGMI0_RXD0	AP26	IO_L7N_T1L_N1_QBC_AD13N_46
	SW2	AJ28	IO_L6P_T0U_N10_AD6P_46
	USB_DAT1	AK28	IO_L6N_T0U_N11_AD6N_46
	SW1	AH27	IO_L5P_T0U_N8_AD14P_46
	USB_DAT0	AH28	IO_L5N_T0U_N9_AD14N_46
AL27	RGMI1_TXD1	AL27	IO_L4P_T0U_N6_DBC_AD7P_46
AL28	RGMI1_TXD0	AL28	IO_L4N_T0U_N7_DBC_AD7N_46
AK26	RGMI1_TXD3	AK26	IO_L3P_T0L_N4_AD15P_46
AK27	RGMI1_TXD2	AK27	IO_L3N_T0L_N5_AD15N_46
AM26	RGMI0_RXD1	AM26	IO_L2P_T0L_N2_46
AM27	RGMI0_RXD2	AM27	IO_L2N_T0L_N3_46
AH26	SW3	AH26	IO_L1P_T0L_N0_DBC_46
AJ26	SW0	AJ26	IO_L1N_T0L_N1_DBC_46
	USB_NXT	AK33	IO_T3U_N12_46
	MDCK1	AH29	IO_T2U_N12_46
		AM31	IO_T1U_N12_46
		AG26	IO_T0U_N12_VRP_46
		AG27	VREF_46

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SOC\_IRONWOOD\_FF1156



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Title		
NUCLEI_HPSOC_XCKU_EVB (HP060)		
Size	Document Number	Rev
A4	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 19 of 28

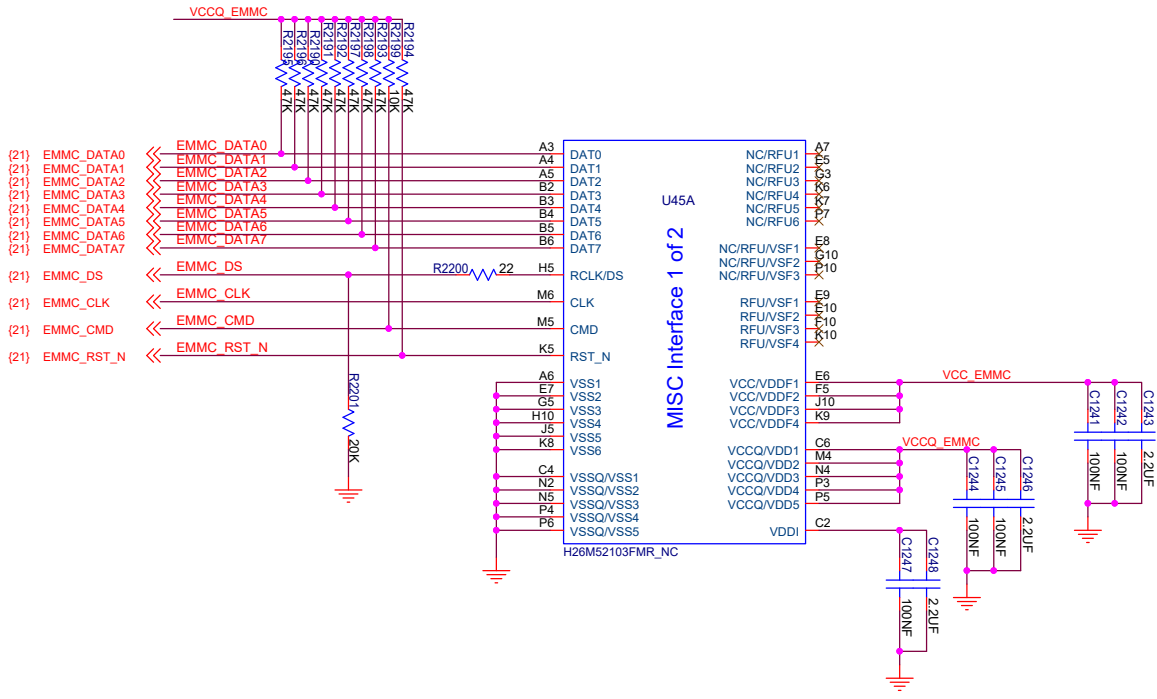


{1^,4} FAN\_PWN  FAN\_PWN

{1^,4} LED[5:0]  LED[5:0]

Title		
NUCLEI_HPSOC_XCKU_EV8 (HP060)		
Size	Document Number	Rev
A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 20 of 28

# EMMC

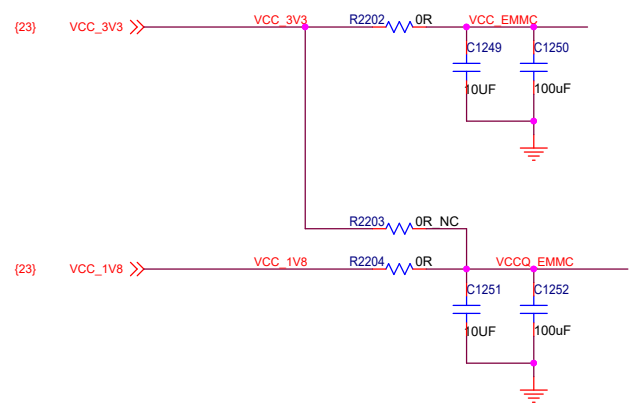


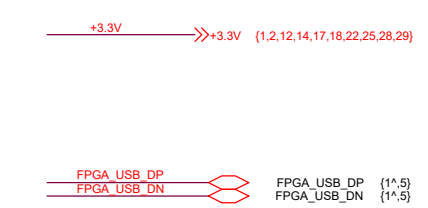
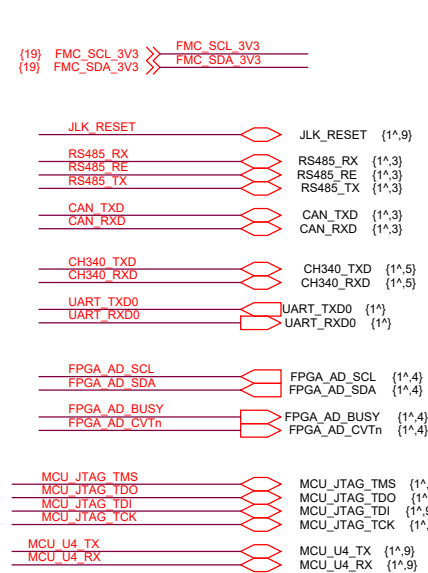
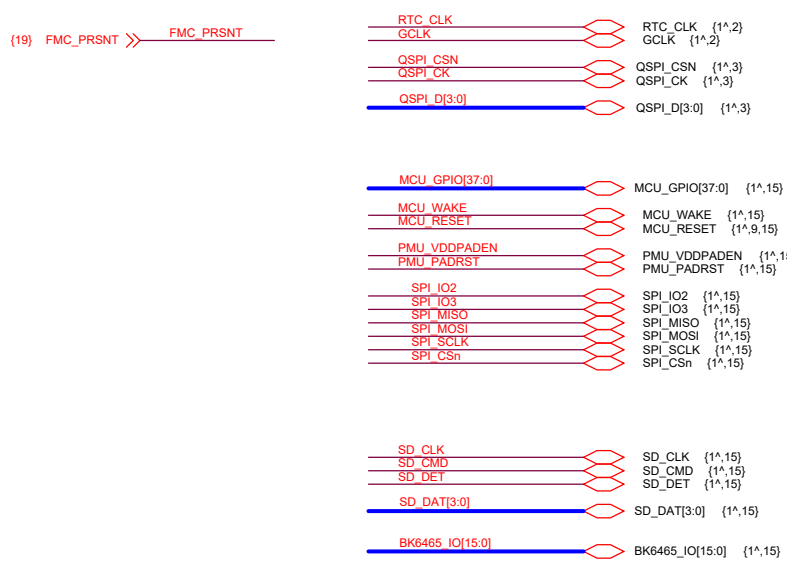
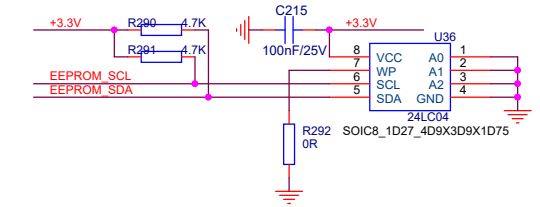
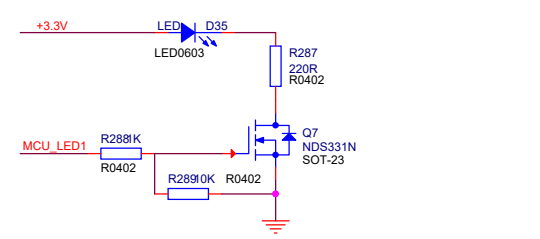
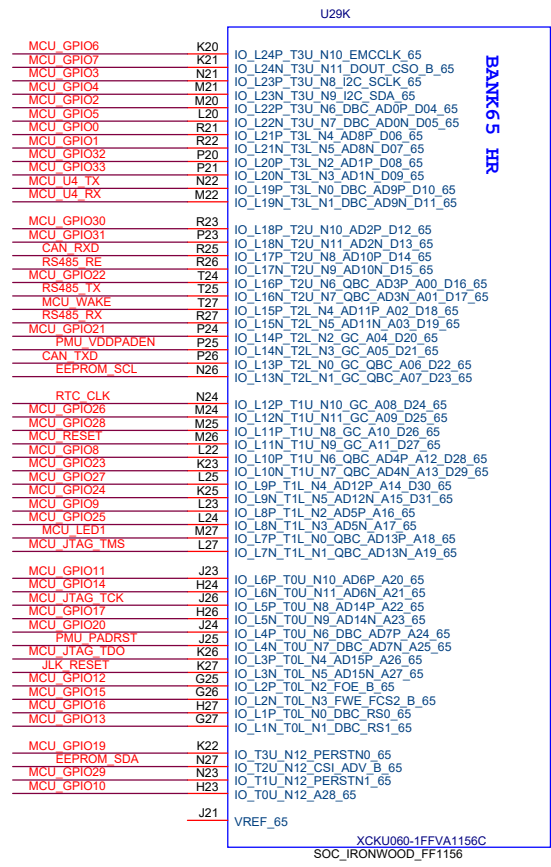
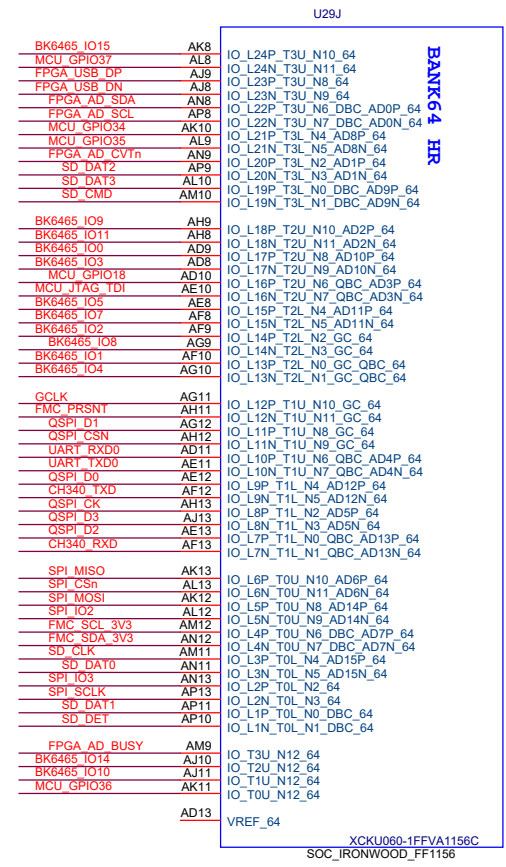
U45B

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A10	NC4	NC58	H14
A11	NC5	NC59	J1
A12	NC6	NC60	J2
A13	NC8	NC61	J3
A14	NC9	NC62	J12
B1	NC10	NC63	J13
B7	NC11	NC64	J14
B8	NC11	NC65	K1
B9	NC12	NC66	K2
B10	NC13	NC67	K3
B11	NC14	NC68	K12
B12	NC15	NC69	K13
B13	NC16	NC70	K14
B14	NC17	NC71	L1
C1	NC18	NC72	L2
C3	NC19	NC73	L3
C7	NC20	NC74	L12
C5	NC21	NC75	L13
C7	NC22	NC76	L14
C9	NC23	NC77	M1
C10	NC24	NC78	M2
C11	NC25	NC79	M3
C12	NC26	NC80	M7
C13	NC27	NC81	M8
C14	NC28	NC82	M9
D1	NC29	NC83	M10
D2	NC30	NC84	M11
D3	NC31	NC85	M12
D4	NC32	NC86	M13
D12	NC34	NC87	M14
D13	NC35	NC88	N1
D14	NC35	NC89	N3
E1	NC36	NC90	N6
E2	NC37	NC91	N7
E3	NC38	NC92	N8
E12	NC39	NC93	N9
E13	NC40	NC94	N10
E14	NC41	NC95	N11
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F2	NC43	NC97	N13
F3	NC44	NC98	N14
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F13	NC46	NC100	P2
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G2	NC49	NC103	P11
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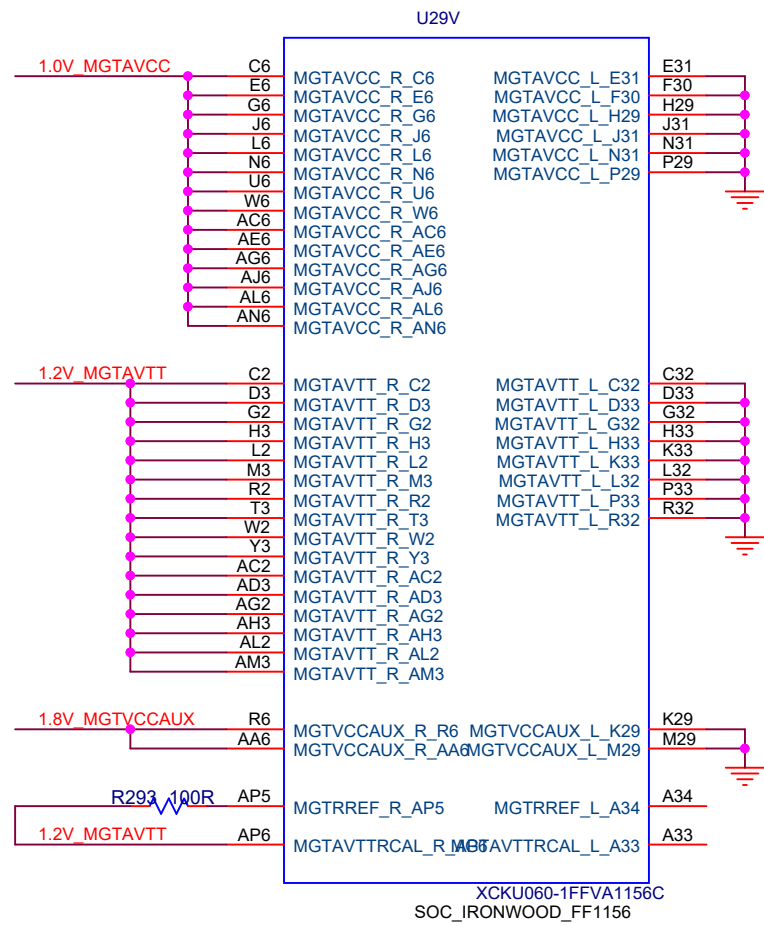
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NC Interface 2 of 2



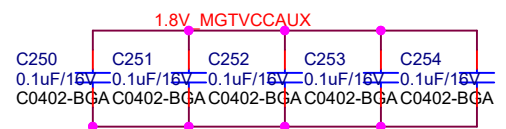
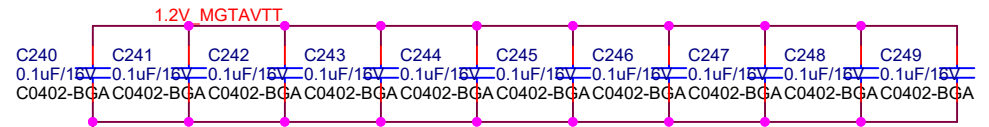
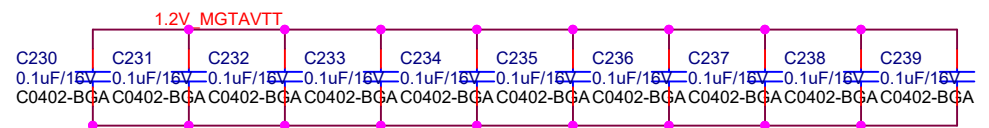
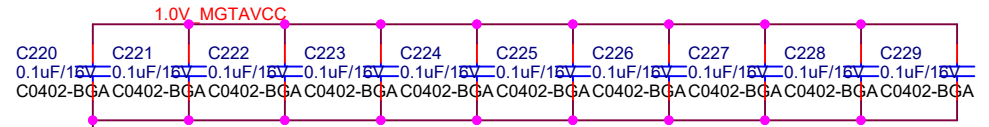
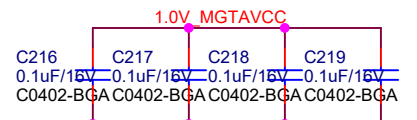
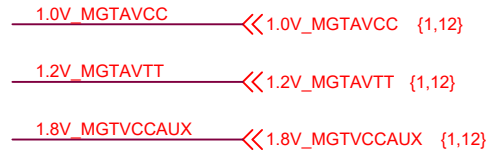


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Size	Document Number	Rev
A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 22 of 28

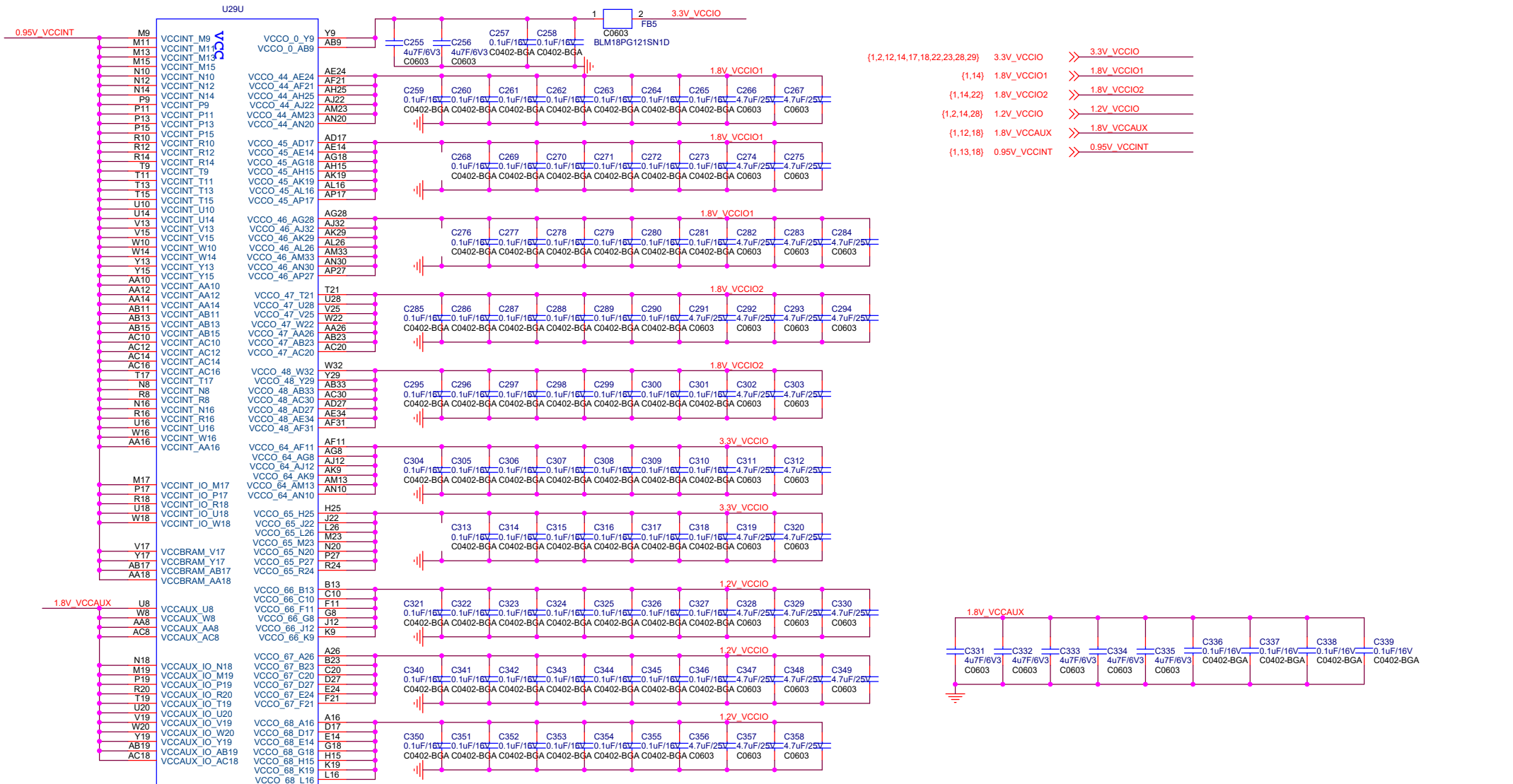


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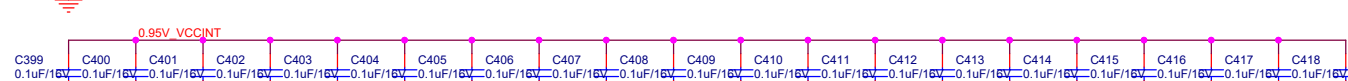
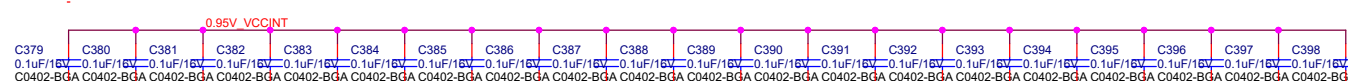
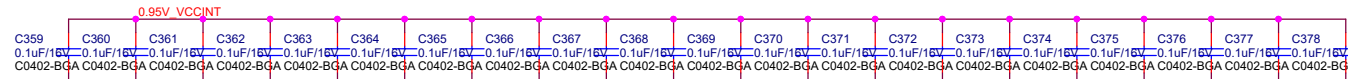
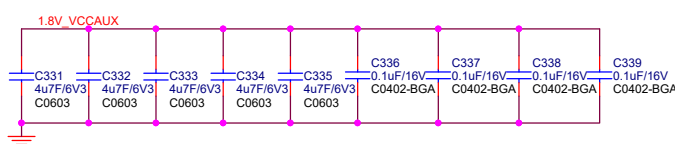
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MGTAVTT: 1.17-1.23V  
MGTVCCAUX: 1.75-1.85V  
MGTAVTTRCAL/MGTTRREF: 1.2V 供电  
接AVTT



Title		
NUCLEI_HPSOC_XCKU_EVB (HP060)		
Size	Document Number	Rev
A4	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 23 of 28



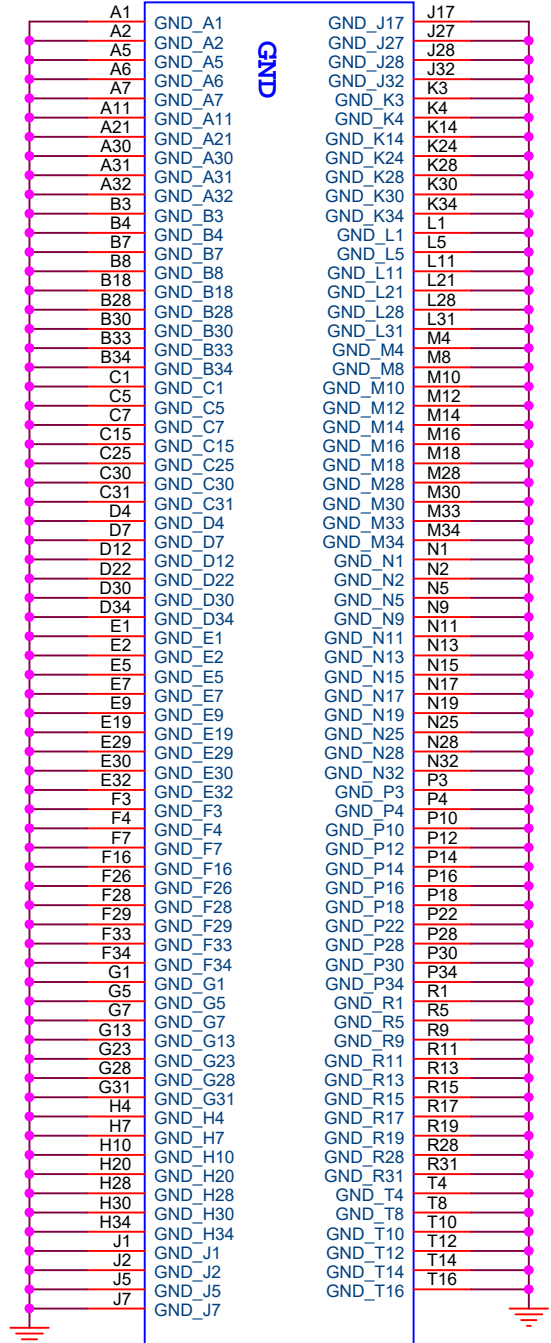
- (1,2,12,14,17,18,22,23,28,29) 3.3V\_VCCIO >> 3.3V\_VCCIO
- (1,14) 1.8V\_VCCIO1 >> 1.8V\_VCCIO1
- (1,14,22) 1.8V\_VCCIO2 >> 1.8V\_VCCIO2
- (1,2,14,28) 1.2V\_VCCIO >> 1.2V\_VCCIO
- (1,12,18) 1.8V\_VCCAUX >> 1.8V\_VCCAUX
- (1,13,18) 0.95V\_VCCINT >> 0.95V\_VCCINT



Title		
NUCLEI_HPSOC_XCKU_EV6 (HP060)		
Size	Document Number	Rev
A3	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 24 of 28

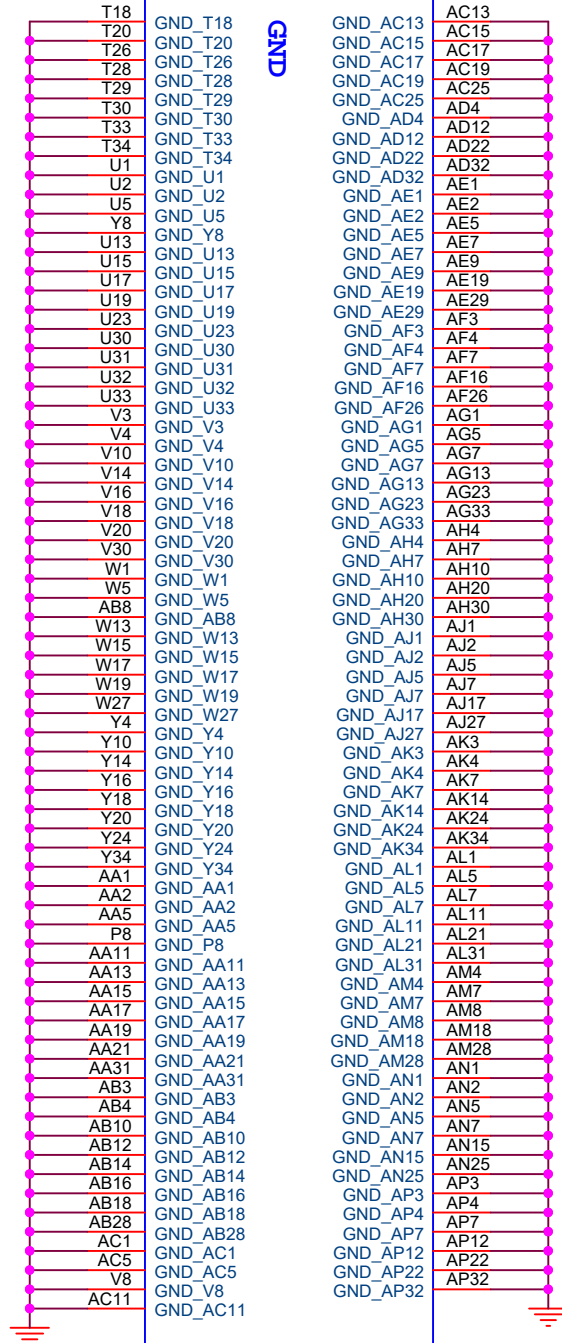


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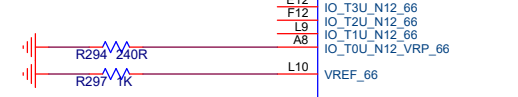
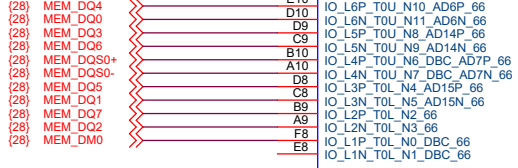
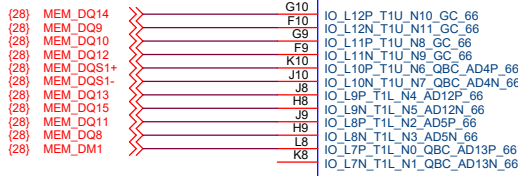
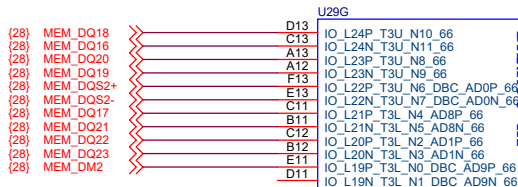
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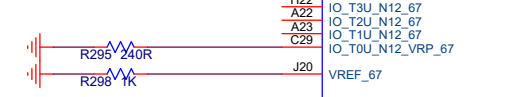
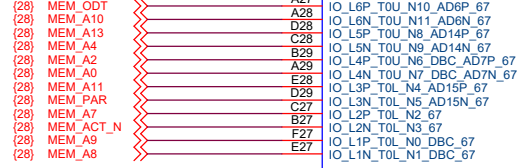
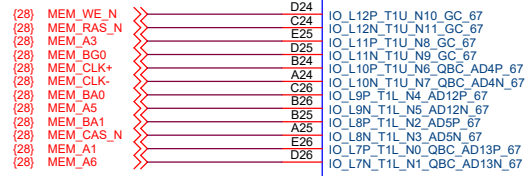
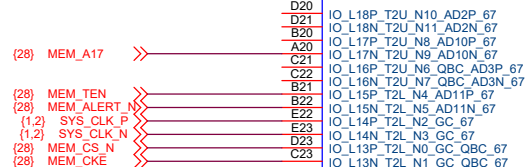
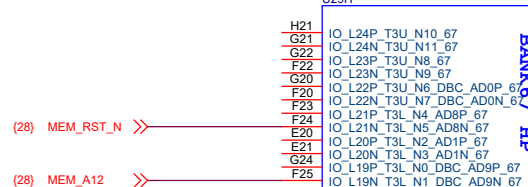


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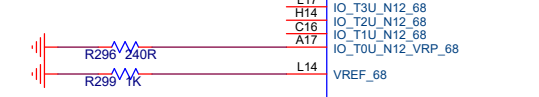
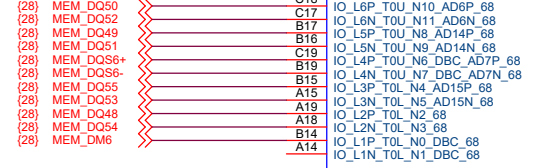
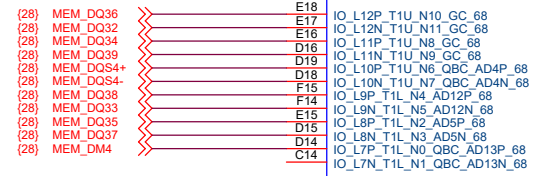
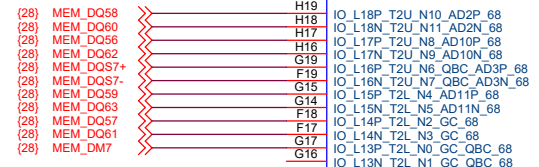
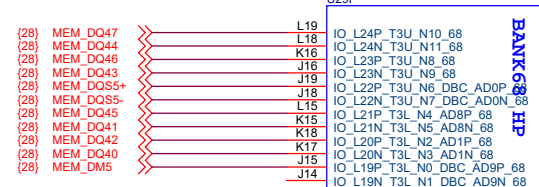
Title		
NUCLEI_HPSOC_XCKU_EVB (HP060)		
Size	Document Number	Rev
A4	<Doc>	1
Date:	Tuesday, October 12, 2021	Sheet 25 of 28



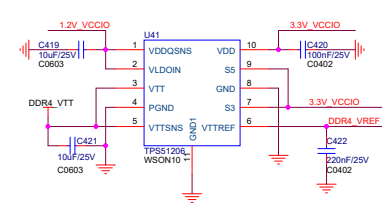
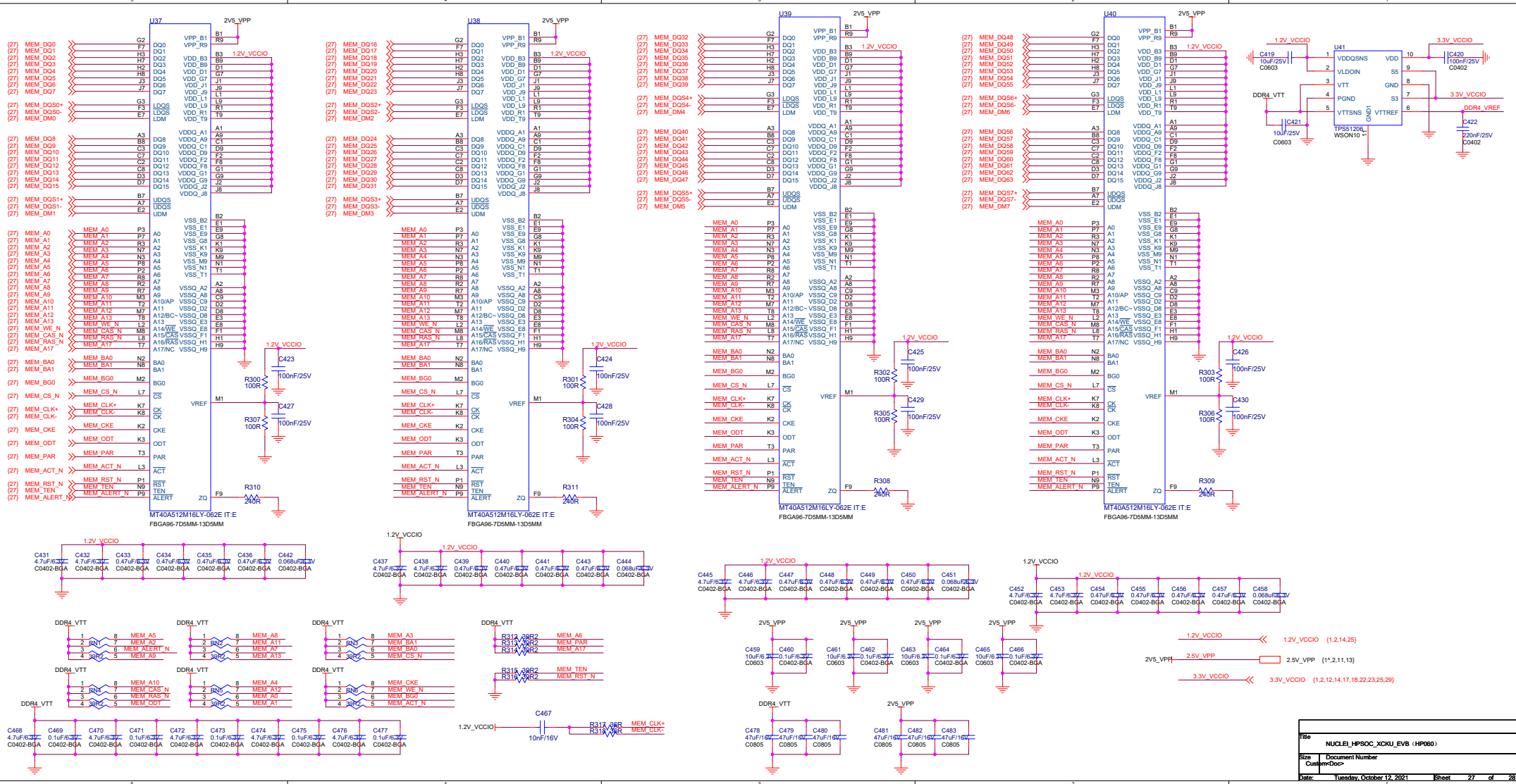
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File	NUCLEI_HPSOC_XCKU_EVB (HP060)	
Size	Document Number	Rev 1
Customer/Doc#		
Date:	Tuesday, October 12, 2021	Sheet 27 of 28

